

## P-Channel 60 V (D-S) MOSFET

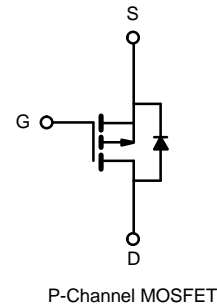
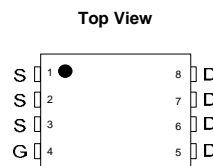
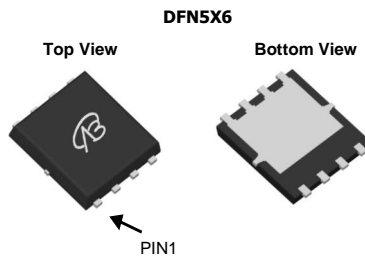
PRODUCT SUMMARY	
$V_{DS}$ (V)	-60
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = -10$ V	0.050
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = -4.5$ V	0.060
$I_D$ (A) per leg	-30

### FEATURES

- TrenchFET® power MOSFET
- 100 %  $R_g$  and UIS tested



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**



ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	$T_C = 25$ °C	-30
		$T_C = 100$ °C	-24
Continuous Source Current (Diode Conduction)	$I_S$	-30	A
Pulsed Drain Current <sup>a</sup>	$I_{DM}$	-90	
Single Pulse Avalanche Current	$I_{AS}$	-22.4	
Single Pulse Avalanche Energy	$E_{AS}$	25	mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_C = 25$ °C	
		$T_C = 125$ °C	1.67
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	$R_{thJA}$	110	°C/W
Junction-to-Foot (Drain)	$R_{thJF}$	30	

### Notes

- Pulse test; pulse width  $\leq 300$   $\mu$ s, duty cycle  $\leq 2$  %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.

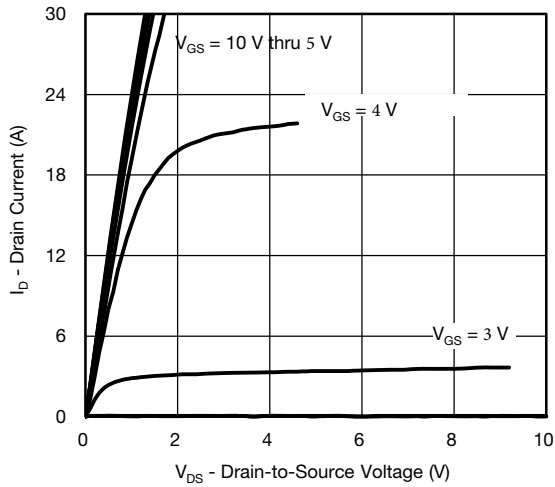
<b>SPECIFICATIONS</b> ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
<b>Static</b>							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-60	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1.0	-	-2.5		
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	$\pm 100$	nA	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{GS} = 0\text{ V}$	$V_{DS} = -60\text{ V}$	-	-	-1	$\mu\text{A}$
		$V_{GS} = 0\text{ V}$	$V_{DS} = -60\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	-50	
		$V_{GS} = 0\text{ V}$	$V_{DS} = -60\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	-150	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{GS} = -10\text{ V}$	$V_{DS} \leq -5\text{ V}$	-30	-	A	
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -10\text{ V}$	$I_D = -4.3\text{ A}$	-	0.050	-	$\Omega$
		$V_{GS} = -10\text{ V}$	$I_D = -4.3\text{ A}, T_J = 125\text{ }^\circ\text{C}$	-	0.075	-	
		$V_{GS} = -10\text{ V}$	$I_D = -4.3\text{ A}, T_J = 175\text{ }^\circ\text{C}$	-	0.125	-	
		$V_{GS} = -4.5\text{ V}$	$I_D = -3.8\text{ A}$	-	0.060	-	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = -15\text{ V}, I_D = -4.3\text{ A}$		-	13	S	
<b>Dynamic<sup>b</sup></b>							
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}$	$V_{DS} = -30\text{ V}, f = 1\text{ MHz}$	-	1000	-	$\text{pF}$
Output Capacitance	$C_{oss}$			-	334	417	
Reverse Transfer Capacitance	$C_{rss}$			-	114	142	
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{GS} = -10\text{ V}$	$V_{DS} = -30\text{ V}, I_D = -5\text{ A}$	-	43.4	65	$\text{nC}$
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			-	4.7	-	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			-	9	-	
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		1.3	2.5	4	$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = -30\text{ V}, R_L = 8.8\text{ }\Omega$ $I_D \cong -5\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		-	11	17	$\text{ns}$
Rise Time <sup>c</sup>	$t_r$			-	11	17	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			-	35	52	
Fall Time <sup>c</sup>	$t_f$			-	6	9	
<b>Source-Drain Diode Ratings and Characteristics<sup>b</sup></b>							
Pulsed Current <sup>a</sup>	$I_{SM}$			-	-	-90	A
Forward Voltage	$V_{SD}$	$I_F = -2.8\text{ A}, V_{GS} = 0\text{ V}$		-	-0.8	-1.2	V

**Notes**

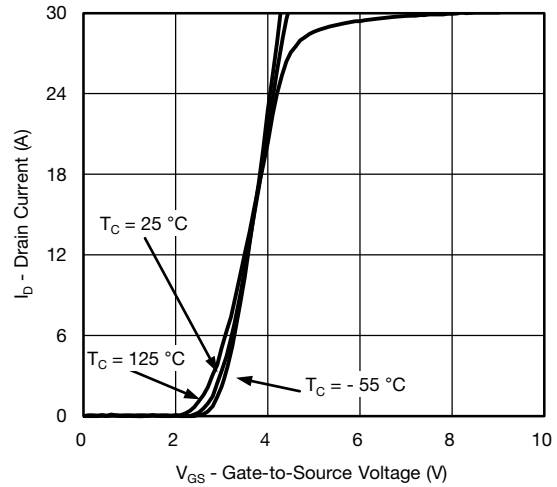
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

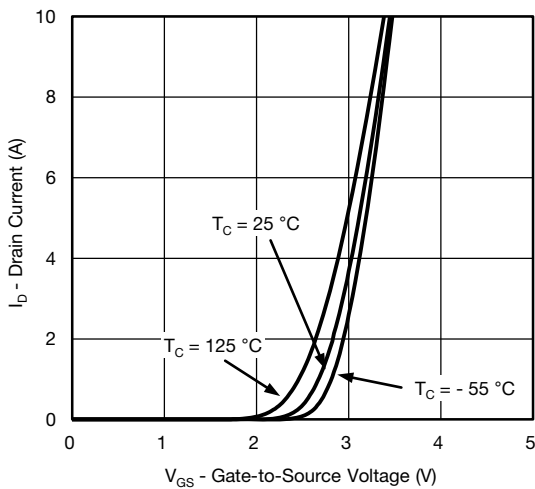
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



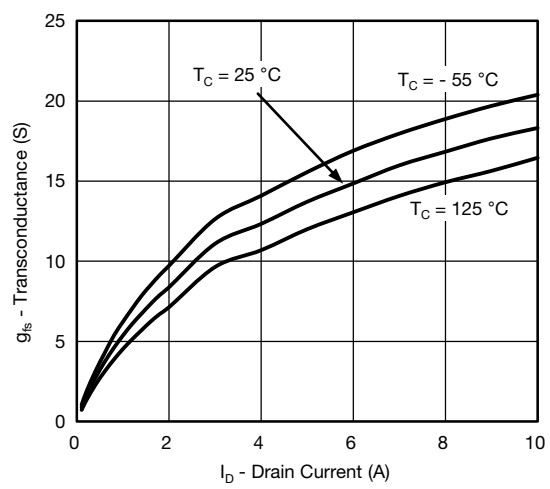
**Output Characteristics**



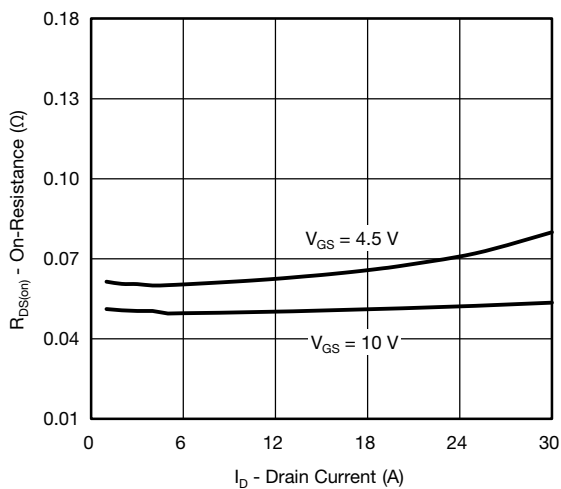
**Transfer Characteristics**



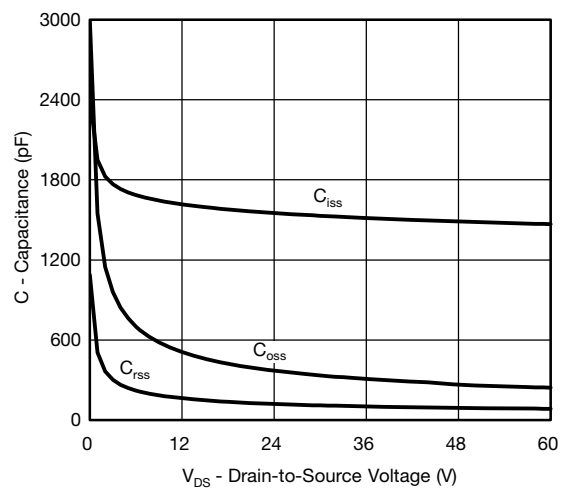
**Transfer Characteristics**



**Transconductance**

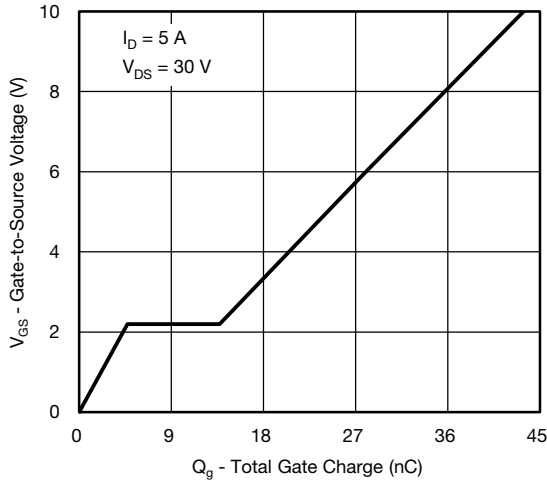


**On-Resistance vs. Drain Current**

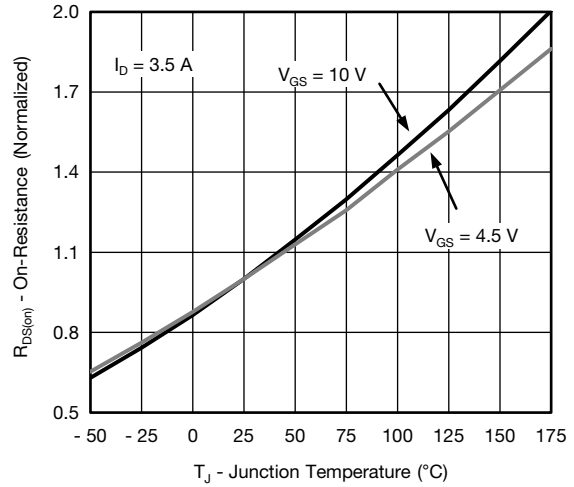


**Capacitance**

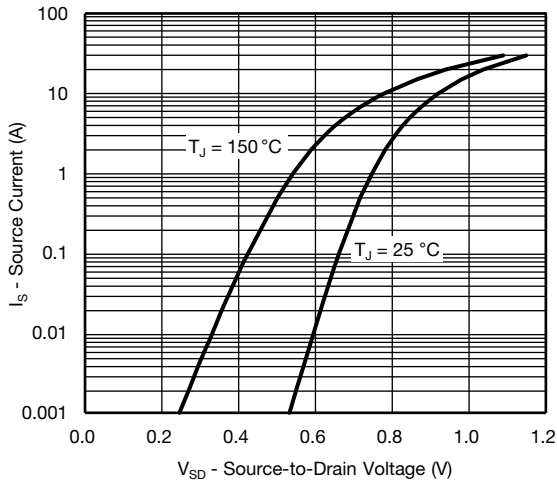
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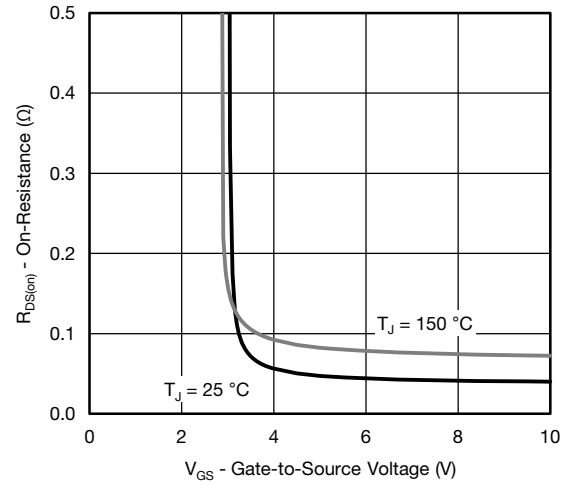
**Gate Charge**



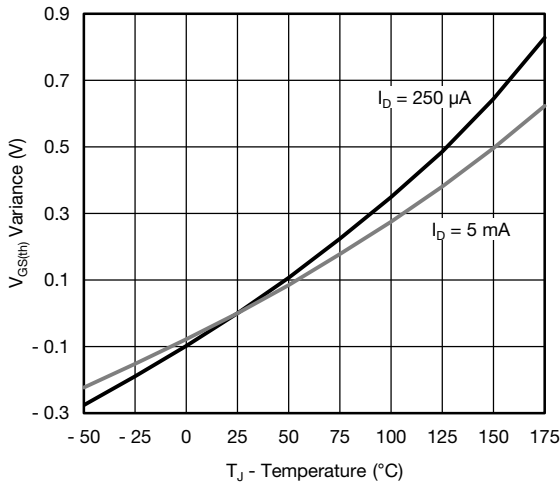
**On-Resistance vs. Junction Temperature**



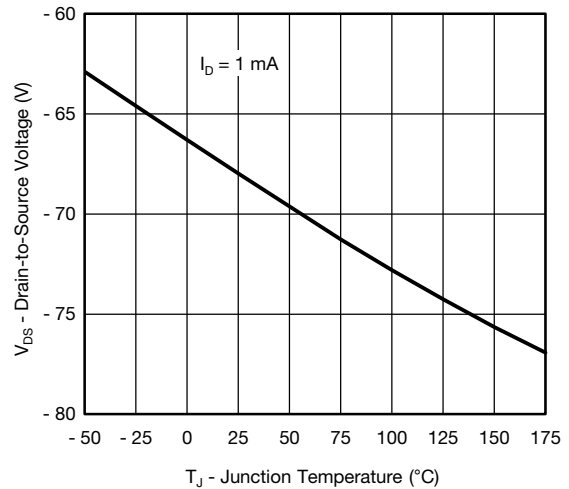
**Source Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**

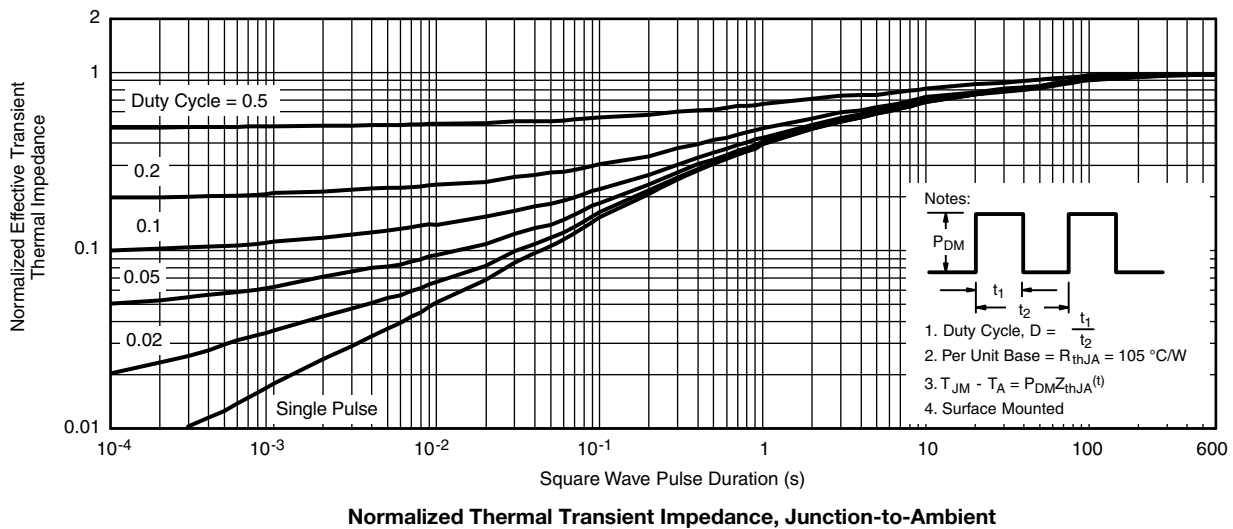
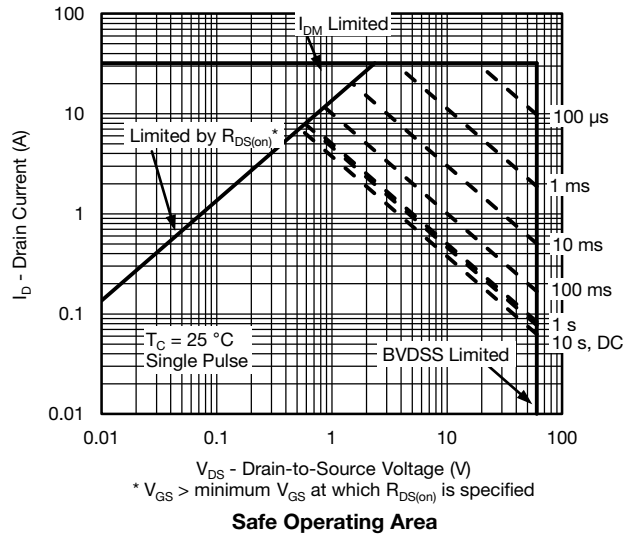


**Threshold Voltage**

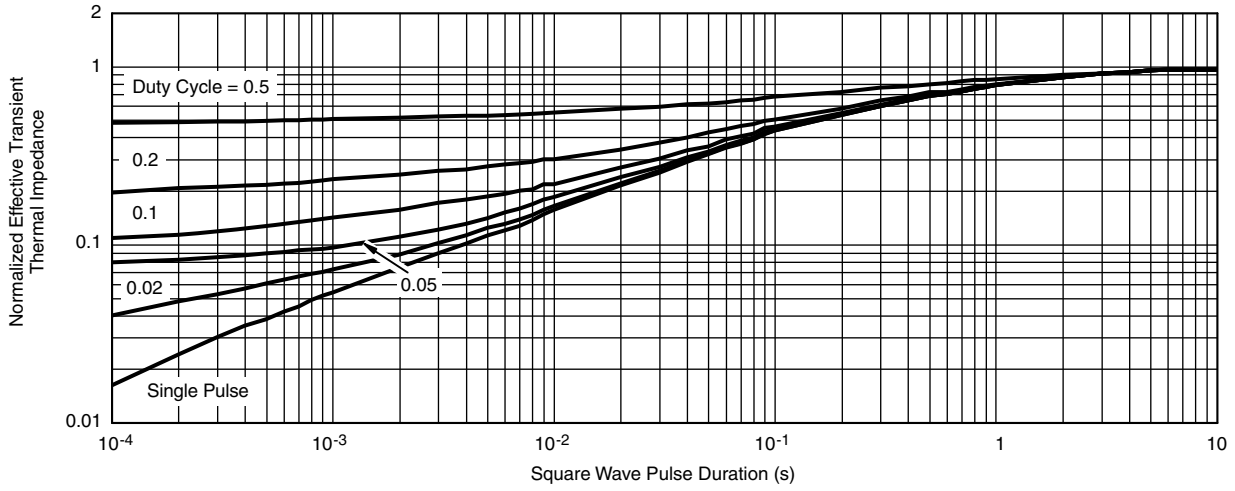


**Drain Source Breakdown vs. Junction Temperature**

**THERMAL RATINGS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



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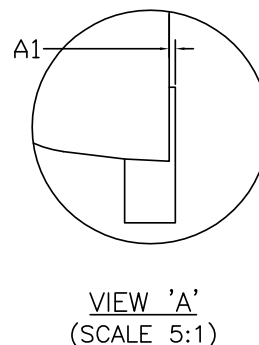
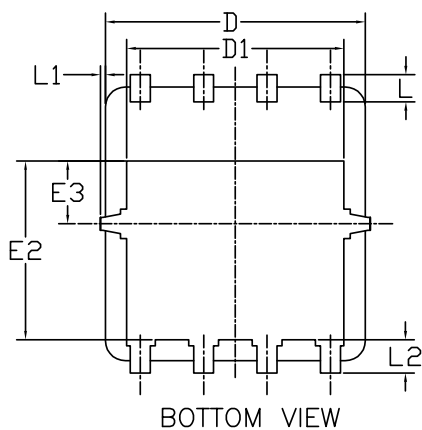
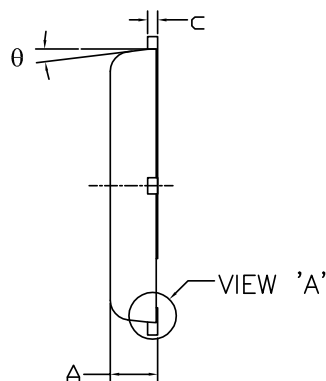
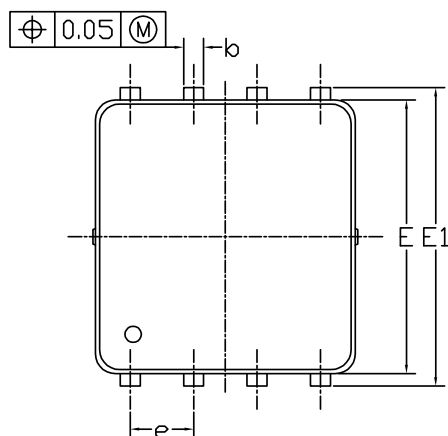


**Normalized Thermal Transient Impedance, Junction-to-Foot**

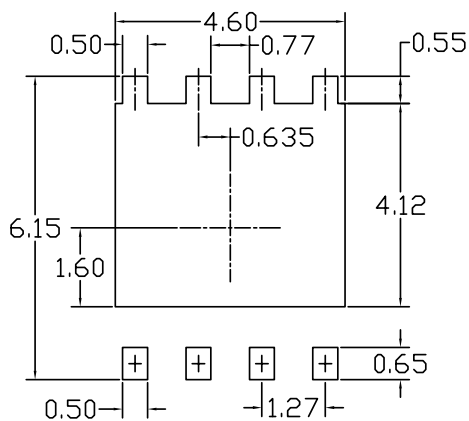
**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

DFN5x6\_8L\_EP1\_P PACKAGE OUTLIN



RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	0.95	1.00	0.033	0.037	0.039
A1	0.00	---	0.05	0.000	---	0.002
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.15	0.20	0.25	0.006	0.008	0.010
D	5.10	5.20	5.30	0.201	0.205	0.209
D1	4.25	4.35	4.45	0.167	0.171	0.175
E	5.45	5.55	5.65	0.215	0.219	0.222
E1	5.95	6.05	6.15	0.234	0.238	0.242
E2	3.525	3.625	3.725	0.139	0.143	0.147
E3	1.175	1.275	1.375	0.046	0.050	0.054
e	1.27 BSC			0.050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026
L1	0	---	0.15	0	---	0.006
L2	0.68 REF			0.027 REF		
θ	0°	---	10°	0°	---	10°

NOTE

UNIT: mm

- PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.  
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- CONTROLLING DIMENSION IS MILLIMETER.  
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT

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