

N-Channel 80 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A)	Q _g (TYP.)	
80	0.001at V _{GS} = 10 V	350	80 nC	



Top View

FEATURES

- SGT technology Power MOSFET
- Maximum 150 °C junction temperature
- 100 % $\rm R_g$ and UIS tested



APPLICATIONS

- Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power supplies
 - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	80	V	
Gate-Source Voltage		V _{GS}	± 20	v	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 25 °C		350	А	
Continuous Drain Current (1) = 130 C)	T _C = 70 °C	I _D	210		
Pulsed Drain Current (t = 100 µs)		I _{DM}	1050	A	
Avalanche Current	L = 0.5 mH	I _{AS}	70		
Single Avalanche Energy ^a	L = 0.5 mm	E _{AS}	1400	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	— P _D	370 ^b	W	
	T _C = 100 °C	r D	180 ^b		
Operating Junction and Storage Temperature Range		TJ, T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	0.4		

Notes

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	80	-	-	v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\;\mu\text{A}$	2.0	-	4.5	
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 250	nA
		$V_{DS}=64~V,~V_{GS}=0~V$	-	-	1	μA
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 64 V, V_{GS} = 0 V, T_J = 125 $^\circ C$	-	-	150	
		$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150^{\circ}\text{C}$	-	-	5	mA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \geq 10 \ V, \ V_{GS} = 10 \ V$	90	-	-	А
	Р	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 50 \text{ A}$	-	0.0010	-	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V_{GS} = 7.5 V, I _D = 30 A	-	0.0075	-	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	75	-	S
Dynamic ^b						
Input Capacitance	C _{iss}		-	15000	-	pF
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 100 V, f = 1 MHz	-	246	-	
Reverse Transfer Capacitance	C _{rss}		-	21	-	
Total Gate Charge ^c	Qg		-	80	96	nC
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 100 V, V_{GS} = 10 V, I_{D} = 60 A	-	16.7	-	
Gate-Drain Charge ^c	Q _{gd}		-	16.9	-	
Gate Resistance	Rg	f = 1 MHz	1.5	3	5	Ω
Turn-On Delay Time ^c	t _{d(on)}		-	15	29	
Rise Time ^c	tr	V_{DD} = 100 V, R _L = 1.66 Ω I _D ≅ 60 A, V _{GEN} = 10 V, R _g = 1 Ω	-	20	25	- ns
Turn-Off Delay Time ^c	t _{d(off)}		-	31	71	
Fall Time ^c	t _f		-	20	35	
Drain-Source Body Diode Ratings and	nd Characteri	stics ^b (T _C = 25 °C)				
Pulsed Current (t = 100 µs)	I _{SM}		-	-	1050	А
Forward Voltage ^a	V _{SD}	$I_F = 10 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.8	1.2	V
Reverse Recovery Time	t _{rr}	I _F = 30 A, di/dt = 100 A/μs	-	180	-	ns
Peak Reverse Recovery Charge	I _{RM(REC)}		-	11	20	А
Reverse Recovery Charge	Q _{rr}		-	0.9	1.8	μC

Notes

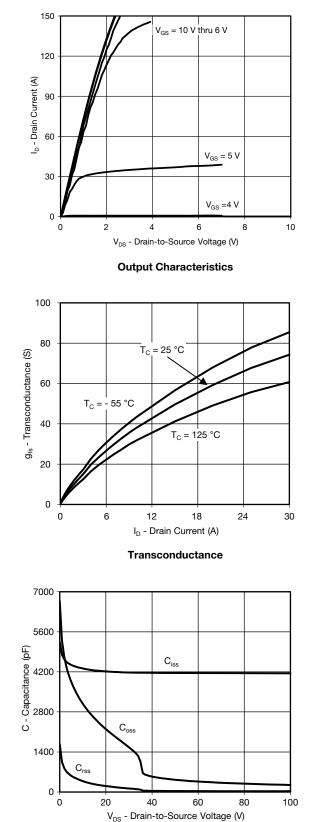
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

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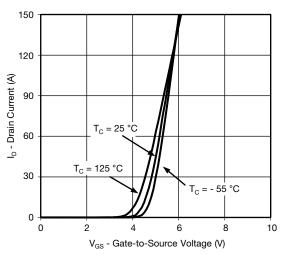
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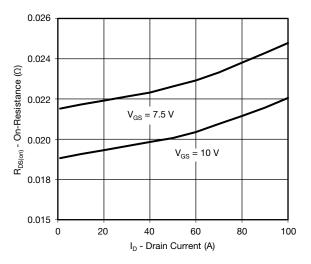
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

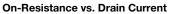


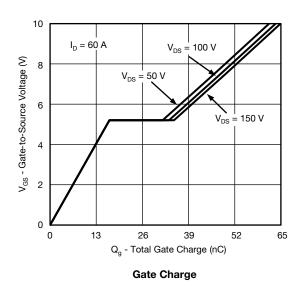
Capacitance



Transfer Characteristics



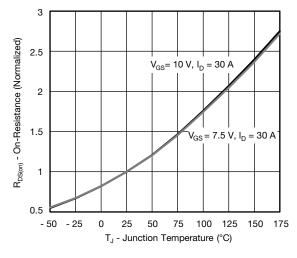




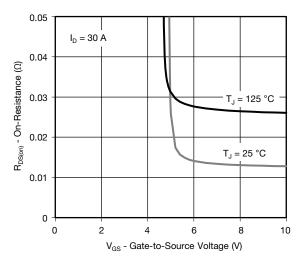
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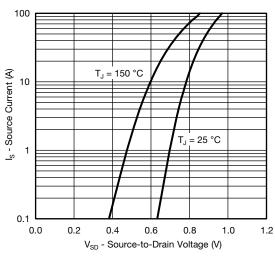
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



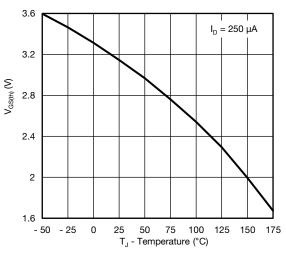
On-Resistance vs. Junction Temperature



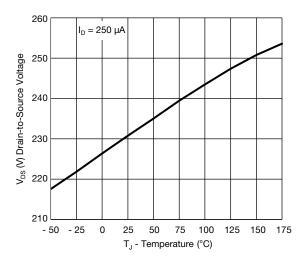
On-Resistance vs. Gate-to-Source Voltage



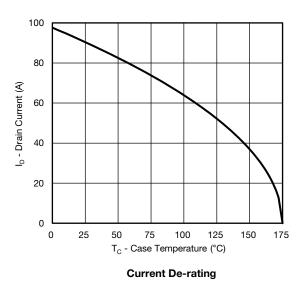
Source Drain Diode Forward Voltage



Threshold Voltage

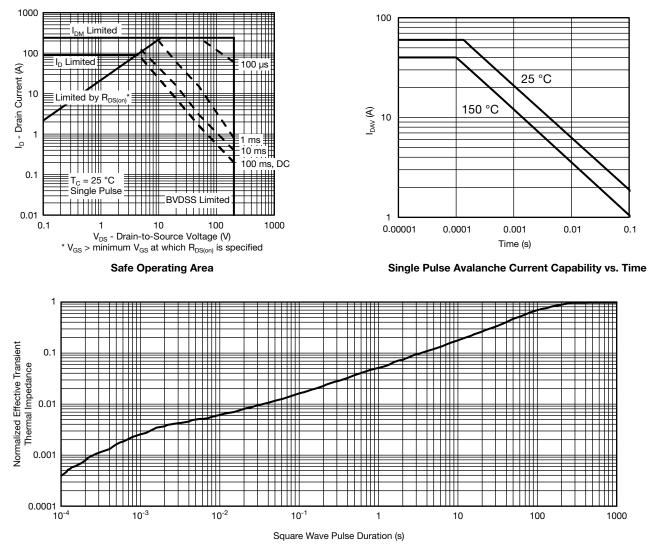


Drain Source Breakdown vs. Junction Temperature





THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

The characteristics shown in the two graphs - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

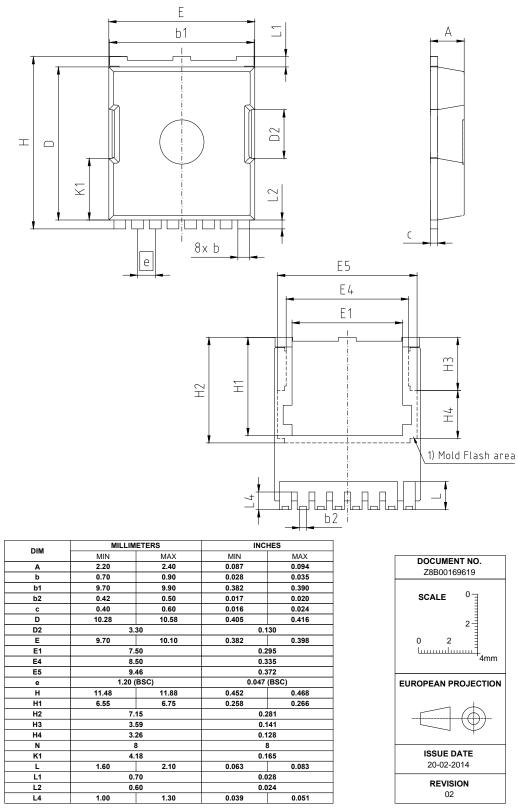
- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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Package Outlines





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