

Dual N-Channel 60 V (D-S) MOSFET

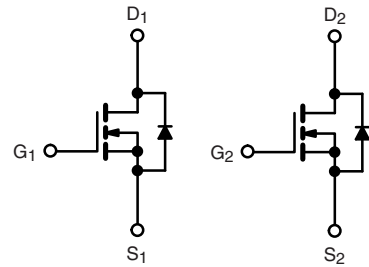
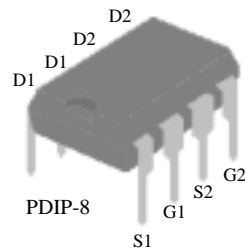
PRODUCT SUMMARY	
V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.033
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.045
I_D (A) per leg	7
Configuration	Dual

FEATURES

- TrenchFET® power MOSFET
- 100 % R_g and UIS tested



RoHS
COMPLIANT
HALOGEN
FREE



N-Channel MOSFET N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	$T_C = 25$ °C	7
		$T_C = 125$ °C	4
Continuous Source Current (Diode Conduction) ^a	I_S	3.6	A
Pulsed Drain Current ^b	I_{DM}	28	
Single Pulse Avalanche Current	I_{AS}	18	
Single Pulse Avalanche Energy	E_{AS}	16.2	mJ
Maximum Power Dissipation ^b	P_D	$T_C = 25$ °C	4
		$T_C = 125$ °C	1.3
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	110	°C/W
Junction-to-Foot (Drain)	R_{thJF}	34	

Notes

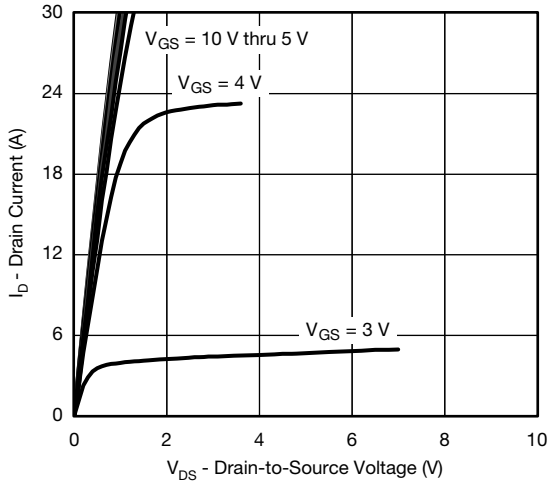
- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR4 material).

SPECIFICATIONS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$		60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$		1.5	2.0	2.5	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$		-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}$	-	-	1	μA
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	50	
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	150	
On-State Drain Current ^a	$I_{D(on)}$	$V_{GS} = 10\text{ V}$	$V_{DS} \geq 5\text{ V}$	20	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$	$I_D = 4.5\text{ A}$	-	0.033	-	Ω
		$V_{GS} = 10\text{ V}$	$I_D = 4.5\text{ A}, T_J = 125\text{ }^\circ\text{C}$	-	0.066	-	
		$V_{GS} = 10\text{ V}$	$I_D = 4.5\text{ A}, T_J = 175\text{ }^\circ\text{C}$	-	0.081	-	
		$V_{GS} = 4.5\text{ V}$	$I_D = 4\text{ A}$	-	0.045	-	
Forward Transconductance ^f	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 4.5\text{ A}$		-	15	-	S
Dynamic ^b							
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}$	$V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	600	750	μF
Output Capacitance	C_{oss}			-	110	140	
Reverse Transfer Capacitance	C_{rss}			-	50	62	
Total Gate Charge ^c	Q_g	$V_{GS} = 10\text{ V}$	$V_{DS} = 30\text{ V}, I_D = 5.3\text{ A}$	-	11.7	18	nC
Gate-Source Charge ^c	Q_{gs}			-	1.8	2.7	
Gate-Drain Charge ^c	Q_{gd}			-	2.8	4.2	
Gate Resistance	R_g	f = 1 MHz		1.3	-	6	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 30\text{ V}, R_L = 6.8\text{ }\Omega$ $I_D \cong 4.4\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		-	7	11	ns
Rise Time ^c	t_r			-	3.3	5	
Turn-Off Delay Time ^c	$t_{d(off)}$			-	22.4	33.5	
Fall Time ^c	t_f			-	2.1	3.2	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed Current ^a	I_{SM}			-	-	28	A
Forward Voltage	V_{SD}	$I_F = 2\text{ A}, V_{GS} = 0\text{ V}$		-	0.75	1.1	V

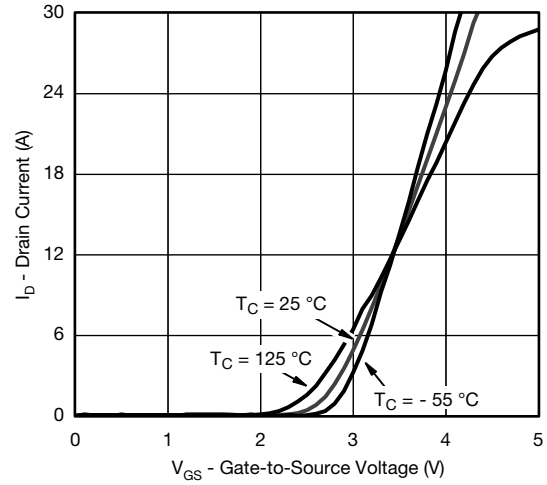
Notes

- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

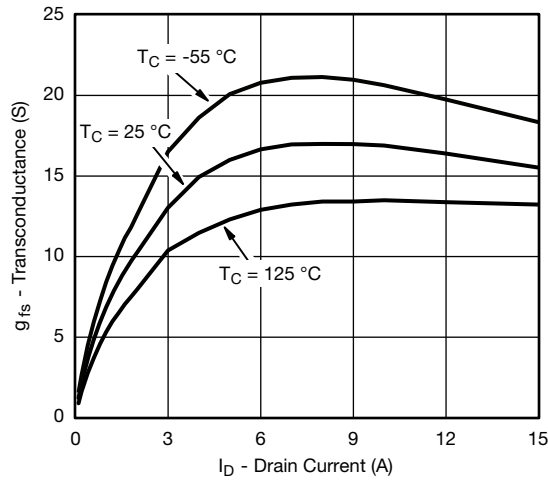
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



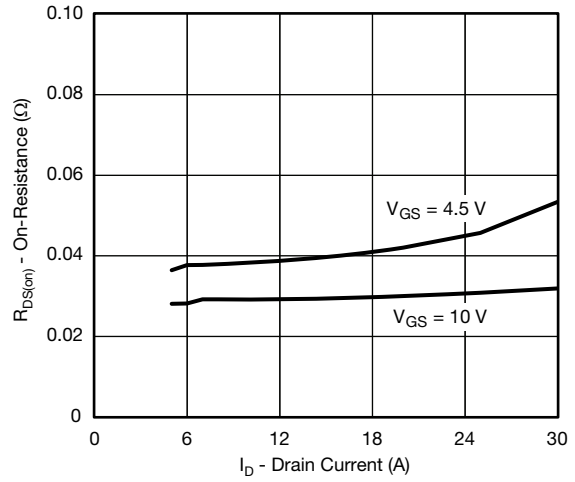
Output Characteristics



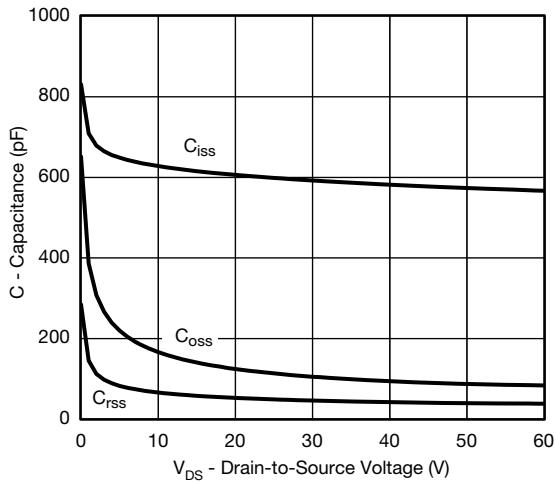
Transfer Characteristics



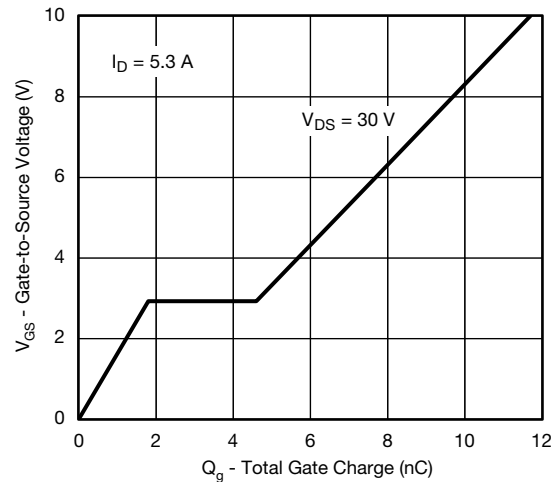
Transconductance



On-Resistance vs. Drain Current

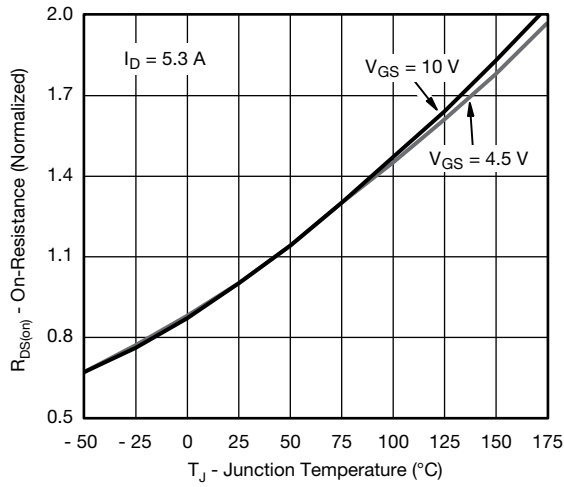


Capacitance

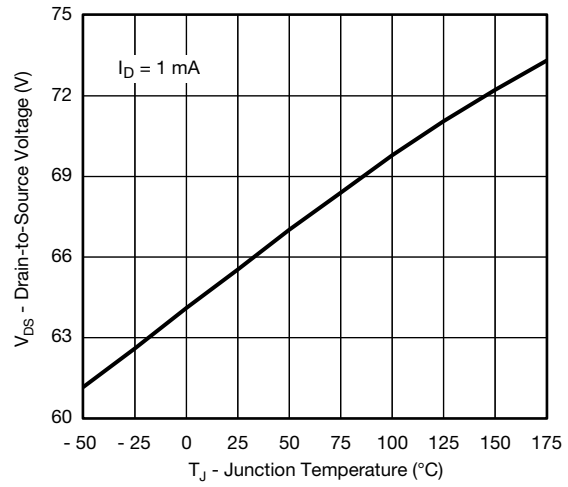


Gate Charge

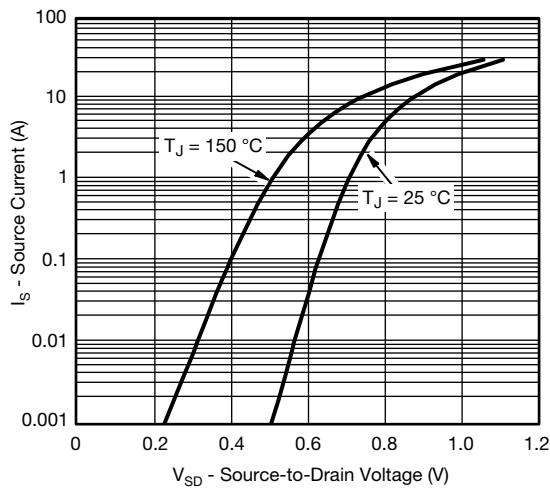
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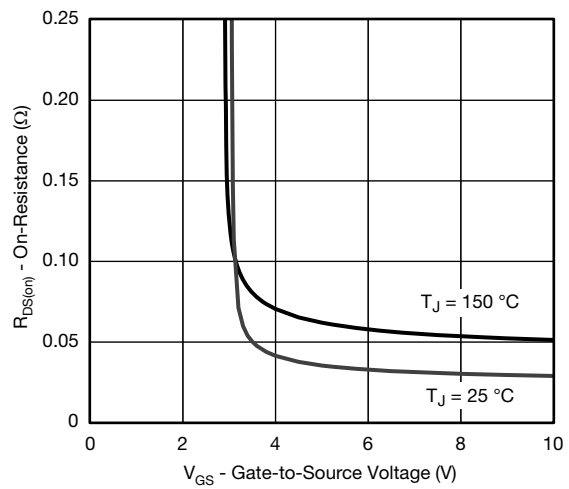
On-Resistance vs. Junction Temperature



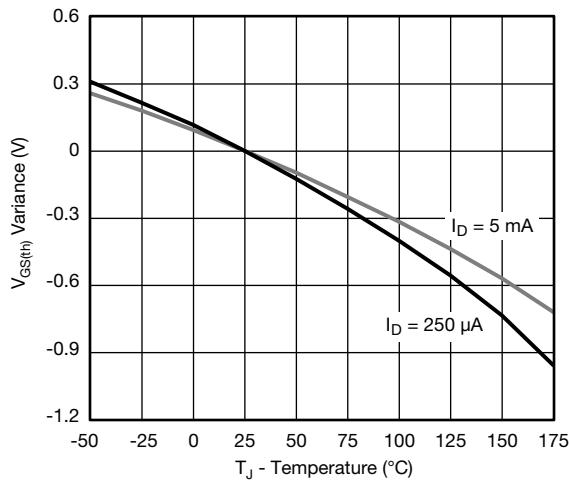
Drain Source Breakdown vs. Junction Temperature



Source Drain Diode Forward Voltage

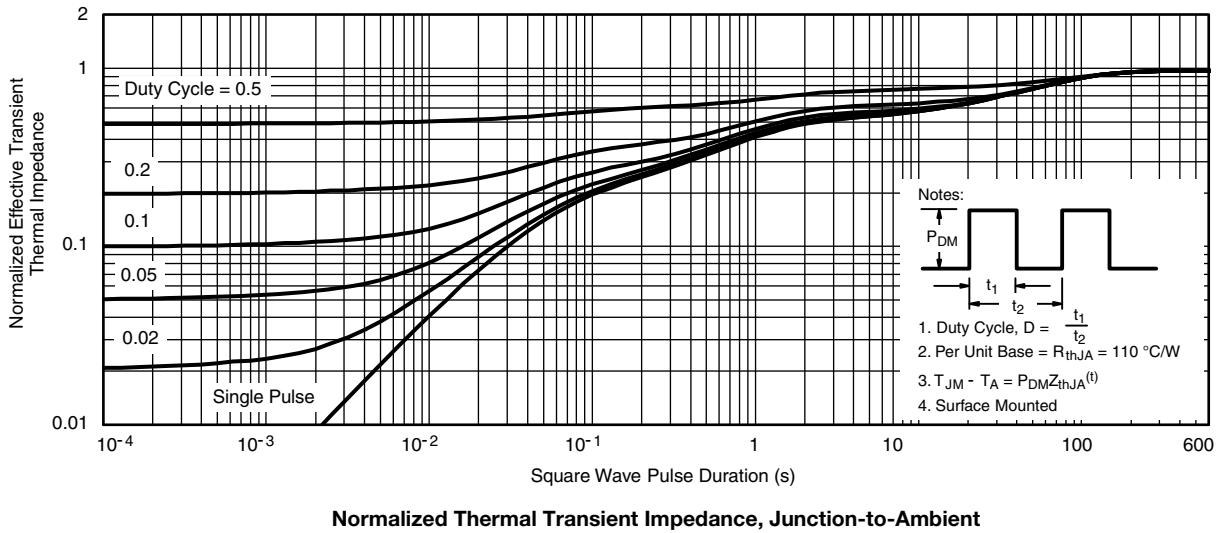
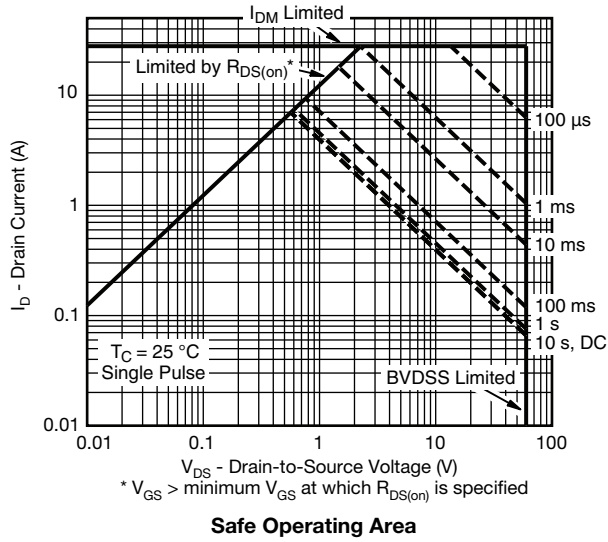


On-Resistance vs. Gate-to-Source Voltage

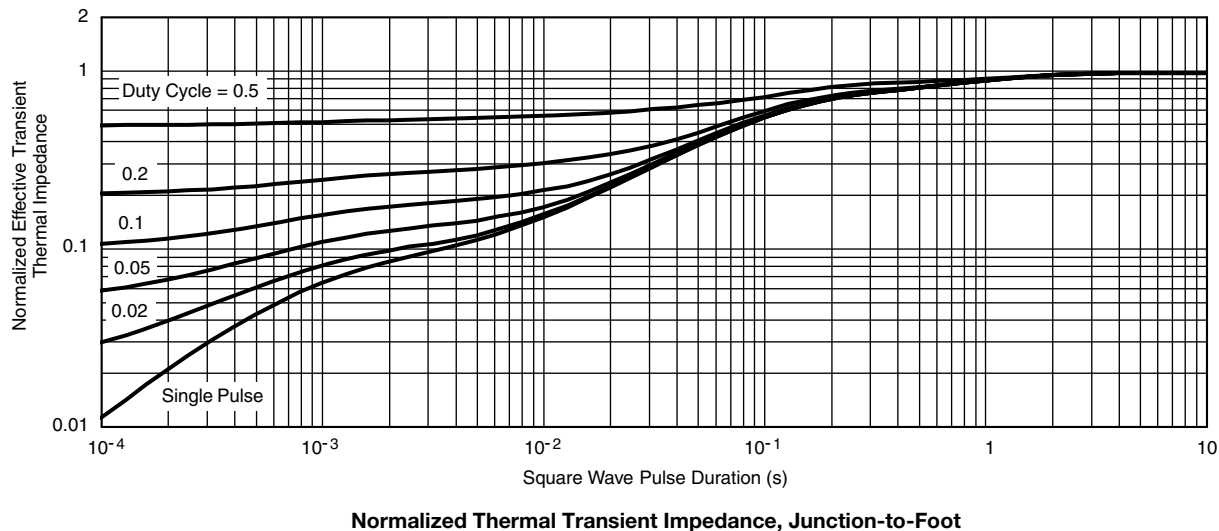


Threshold Voltage

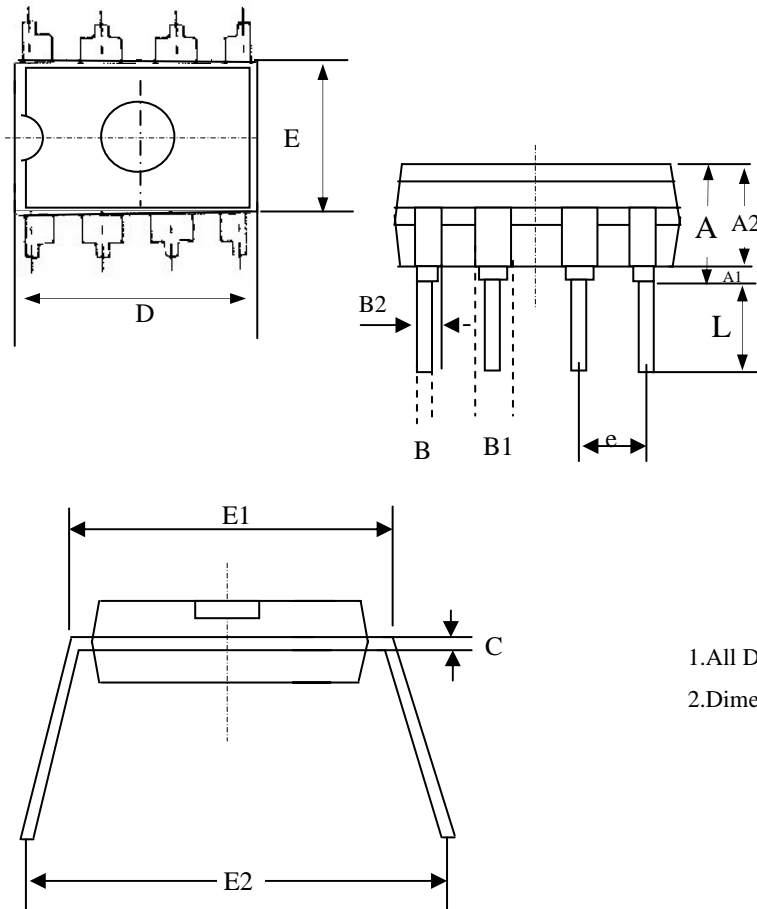
THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Package Outline : PDIP-8



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	3.60	4.50	5.40
A1	0.38	----	----
A2	2.90	3.95	5.00
B	0.36	0.46	0.56
B1	1.10	1.45	1.80
B2	0.76	0.98	1.20
C	0.20	0.28	0.36
D	9.00	9.60	10.20
E	6.10	6.65	7.20
E1	7.62	7.94	8.26
E2	8.30	9.65	11.00
e	2.540 BSC		
L	3.18	----	----

- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

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