

## P-Channel 150 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$ $I_{D}(A)$		Q <sub>g</sub> (Typ.)		
- 150	0.270at V <sub>GS</sub> = - 10 V	- 10	11.7		
- 130	0.280 at V <sub>GS</sub> = - 4.5 V	- 8	11.7		

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R<sub>q</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



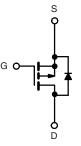
#### **APPLICATIONS**

- Power Switch
- DC/DC Converters



TO-252

Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>C</sub> = 25 °C, unless otherwise noted						
Parameter			Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 150				
Gate-Source Voltage	$V_{GS}$	± 20	V			
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 25 °C	l <sub>a</sub>	- 10	A		
Continuous Diam Guiterit (1) = 150 C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	- 8			
Pulsed Drain Current	I <sub>DM</sub>	- 30	A			
Avalanche Current		I <sub>AS</sub>	- 18			
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	16.2	mJ		
	T <sub>C</sub> = 25 °C	В	32.1 <sup>b</sup>	W		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	- P <sub>D</sub>	2.5			
Operating Junction and Storage Temperature R	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	50	°C/W		
Junction-to-Case (Drain)	R <sub>thJC</sub>	3.9	C/VV		

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub> V <sub>DS</sub> = 0 V, I <sub>D</sub> = - 250 μA - 150				V		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 2.5	7 V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA	
		V <sub>DS</sub> = - 120 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			- 50	μΑ	
		V <sub>DS</sub> = - 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			- 250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 15			Α	
Dunin Course On Chata Basistanas	B	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3.6 A		0.270		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.4 A		0.280			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 3.6 A		12		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			1000		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = -75 \text{ V}, f = 1 \text{ MHz}$		65			
Reverse Transfer Capacitance	C <sub>rss</sub>	]		41			
Total Gate Charge <sup>c</sup>	Qg	$V_{DS} = -75 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -3.6 \text{ A}$		23.2	34.8	nC	
Total Gate Charge				11.7	17.6		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = -75 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.6 \text{ A}$		3.5			
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			4.8			
Gate Resistance	$R_g$	f = 1 MHz	1.2	5.7	11.5	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			7	14		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -75 \text{ V}, R_L = 17.2 \Omega$		12	18	nc	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ - 2.9 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		33	50	- ns	
Fall Time <sup>c</sup>	t <sub>f</sub>			9	18		
Drain-Source Body Diode Ratings a	nd Characteri	istics T <sub>C</sub> = 25 °C <sup>b</sup>					
Continuous Current	I <sub>S</sub>	I <sub>S</sub>			- 10	۸	
Pulsed Current	I <sub>SM</sub>				- 30	Α	
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = - 2.9 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			25	50	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = - 2.9 A, dl/dt = 100 A/μs		- 4	- 6	Α	
Reverse Recovery Charge	Q <sub>rr</sub>	1		98	147	nC	

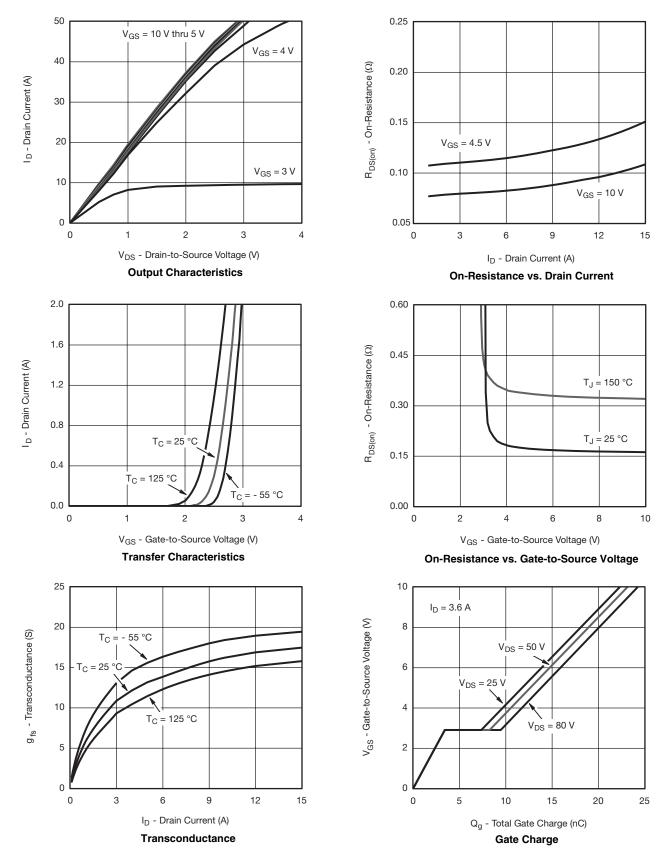
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

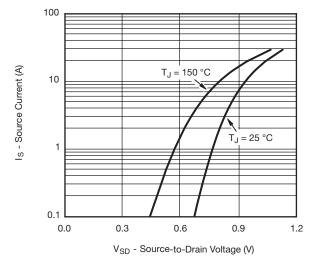


#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

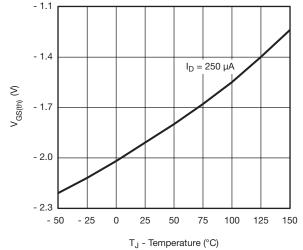




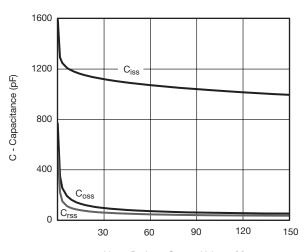
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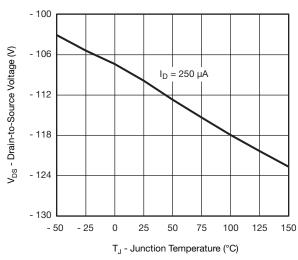
Source-Drain Diode Forward Voltage



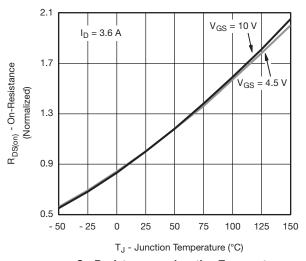
Threshold Voltage



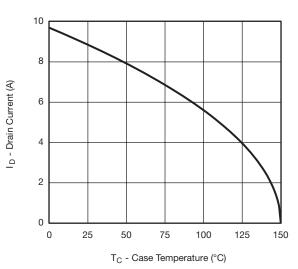
 $V_{DS}$  - Drain-to-Source Voltage (V)  $\label{eq:capacitance}$ 



**Drain Source Breakdown vs. Junction Temperature** 



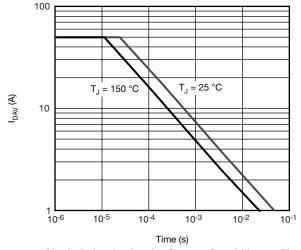
On-Resistance vs. Junction Temperature

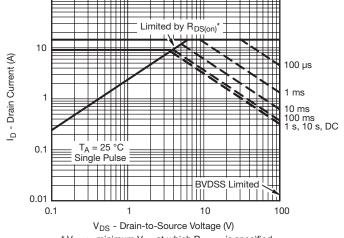


**Current Derating** 



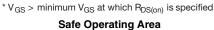
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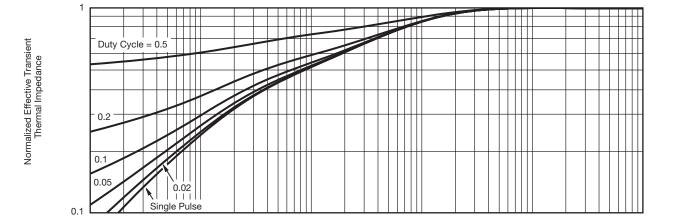




Single Pulse Avalanche Current Capability vs. Time

10-3





10-2

Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Case

10-1

服务热线:400-655-8788

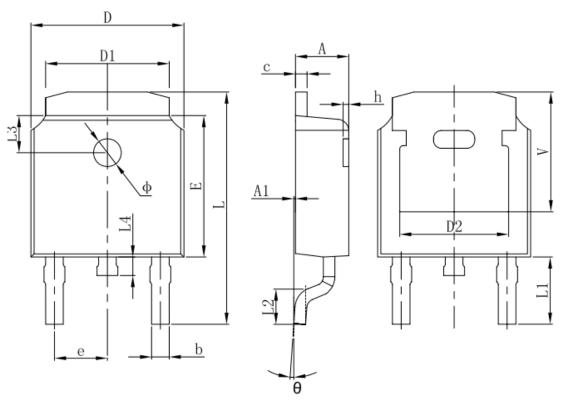
10-4

5

10



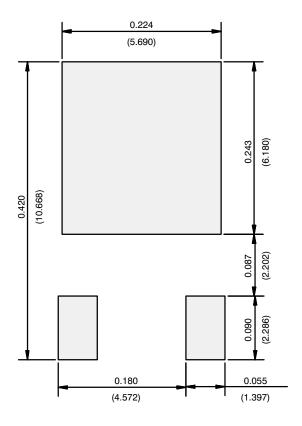
# **TO252 Package Information**



Cumb al	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 REF.		0.190	REF.	
Е	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900 REF.		0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250 REF.		0.207 REF.		



### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)



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