

Dual P-Channel 60-V (D-S) MOSFET

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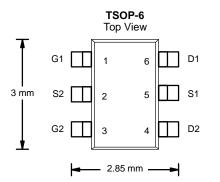
PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) Typ.	I _D (A) ^d	Q _g (TYP.)			
-60	0.070 at V _{GS} = -10 V	-4.5	10.1 nC			
	0.085 at V _{GS} = -4.5 V	-4.0	10.1110			

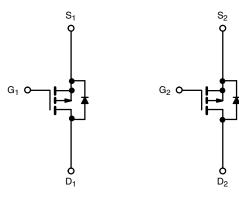
FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET



Compliant to RoHS Directive 2002/95/EC





P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (TA	= 25 °C, unless other	wise noted)		
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	-60	V
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		-4.5	
Continuous Drain Current (T. 150 °C)	T _C = 70 °C		-4.0	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	-3.5 ^{a,b}	
	T _A = 70 °C		-3.0 ^{a,b}	<u>^</u>
Pulsed Drain Current (t = 100 µs)	I _{DM}	-20	— A	
Cartinuaria Causa Duria Dia da Currant	T _C = 25 °C		-3.9	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	-2.1 ^{a,b}	
Avalanche Current		I _{AS}	-15	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	11.25	mJ
	T _C = 25 °C		4.2	
Manimum Davies Diagingtian	T _C = 70 °C	n n	2.7	10/
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^{a,b}	W
	T _A = 70 °C		1.3 ^{a,b}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT			
Maximum junction-to-ambient ^a	t ≤ 5 s	R _{thJA}	100	130	°C/W		
Maximum junction-to-case (drain)	Steady state	R _{thJF}	60	75	0/10		

Notes

a. Surface mounted on 1" x 1" FR4 board.

b. t = 10 s.

- c. Maximum under steady state conditions is 110 °C/W.
- d. Based on $T_C = 25$ °C.

VB4610N

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$	-60	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	L 050 A		-6.7	-	m)//%
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	4.3	-	mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	-1	-	-3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 100	nA
		V _{DS} = -60 V, V _{GS} = 0 V	-	-	-1	μA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-5	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$	-30	-	-	Α
		V _{GS} = -10 V, I _D = -3.5 A	-	0.070	-	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = -4.5 V, I _D = -2.8 A	-	0.085	-	
Forward Transconductance ^a	9 _{fs}	V _{DS} = -30 V, I _D = -3.5 A	-	11	-	S
Dynamic ^b						
Input Capacitance	C _{iss}		-	832	-	pF
Output Capacitance	C _{oss}	V _{DS} = -30 V, V _{GS} = 0 V, f = 1 MHz	-	88	-	
Reverse Transfer Capacitance	C _{rss}		-	63	-	
T + 1 0 + 01		$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = -10 \text{ V}, \text{ I}_{D} = -3.5 \text{ A}$	-	20	30	nC
Total Gate Charge	Qg		-	10.1	15.2	
Gate-Source Charge	Q _{gs}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -3.5 \text{ A}$	-	3.3	-	
Gate-Drain Charge	Q _{gd}		-	3.9	-	
Gate Resistance	Rg	f = 1 MHz	1.8	9	18	Ω
Turn-On Delay Time	t _{d(on)}		-	8	16	-
Rise Time	tr	$V_{DD} = -30 \text{ V}, \text{ R}_{L} = 10.7 \Omega$	-	6	12	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ -2.8 A, V_{GEN} = -10 V, R_g = 1 Ω	-	35	53	
Fall Time	t _f		-	16	24	
Turn-On Delay Time	t _{d(on)}		-	40	60	ns
Rise Time	tr	$V_{DD} = -30 \text{ V}, \text{ R}_{\text{I}} = 10.7 \Omega$	-	28	42	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -2.8$ A, $V_{GEN} = -4.5$ V, $R_g = 1 \Omega$	-	31	47	
Fall Time	t _f		-	15	23	
Drain-Source Body Diode Characterist	ics					
Continous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	-3.5	
Pulse Diode Forward Current (t = 100 µs)	I _{SM}		-	-	-20	A
Body Diode Voltage	V _{SD}	$I_{\rm S}$ = -2.8 A, $V_{\rm GS}$ = 0 V	-	-0.85	-1.2	V
Body Diode Reverse Recovery Time	t _{rr}		-	32	48	ns
Body Diode Reverse Recovery Charge	Q _{rr}	l _F = -2.8 A, dl/dt = 100 A/μs,	-	45	68	nC
Reverse Recovery Fall Time	ta	$T_J = 25 \ ^\circ C$	-	24	-	
Reverse Recovery Rise Time	t _b			8	-	ns

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$

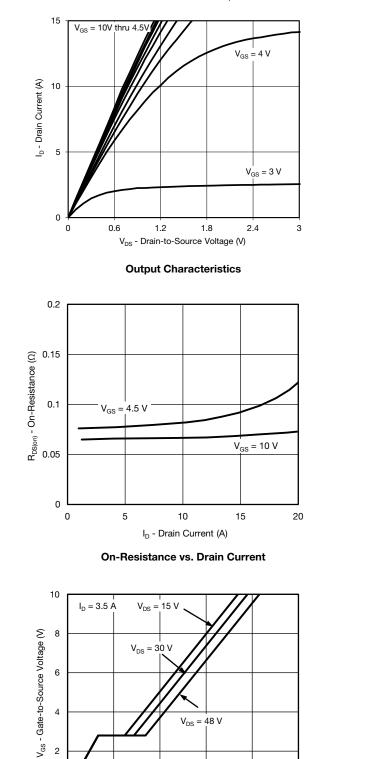
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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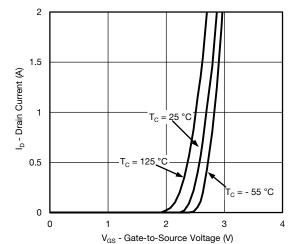
 $V_{DS} = 48 V$

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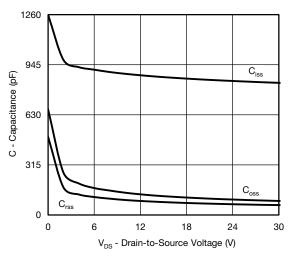
25

20

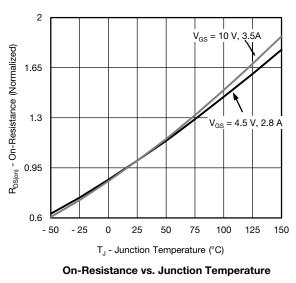
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Transfer Characteristics







服务热线:400-655-8788

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Q_g - Total Gate Charge (nC)

Gate Charge

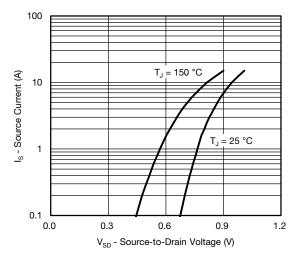
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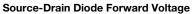
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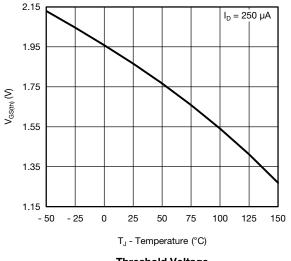
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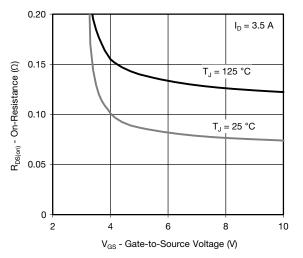


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

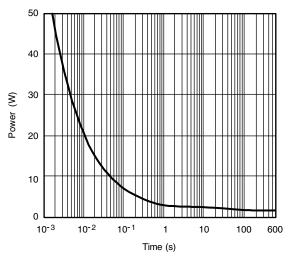




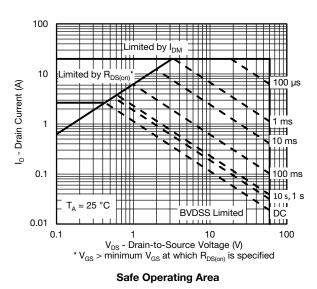




On-Resistance vs. Gate-to-Source Voltage

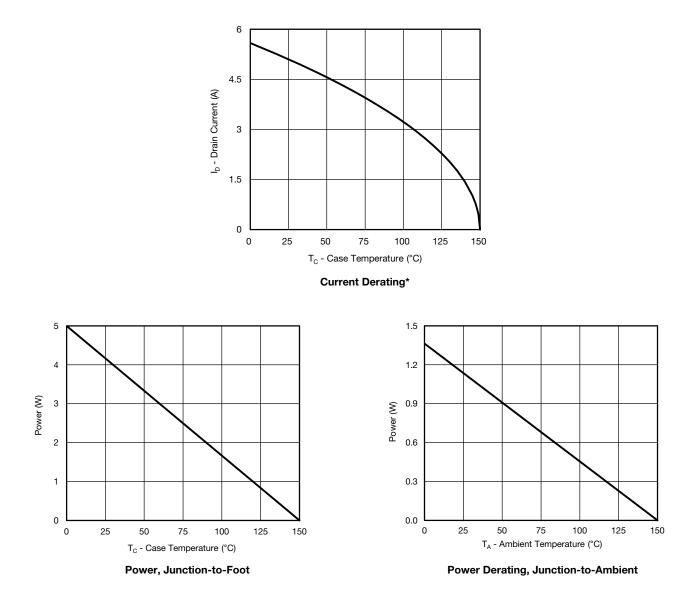


Single Pulse Power, Junction-to-Ambient





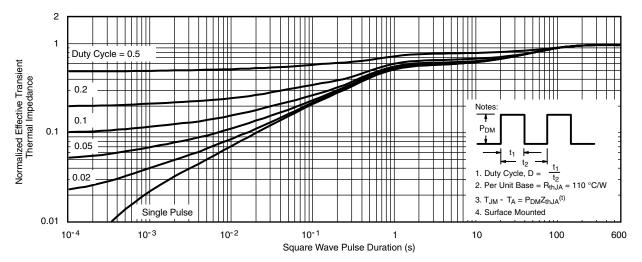
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



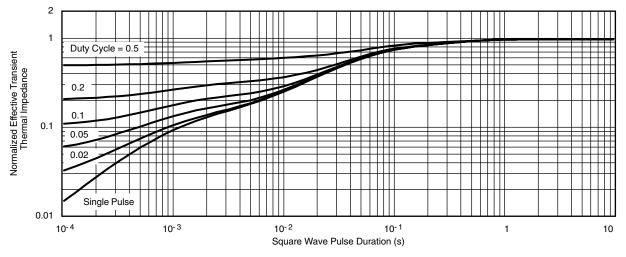
* The power dissipation P_D is based on $T_{J (max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



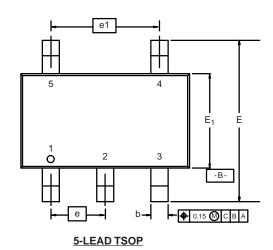
Normalized Thermal Transient Impedance, Junction-to-Ambient

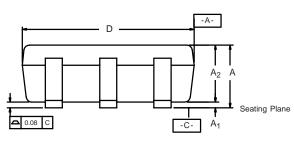


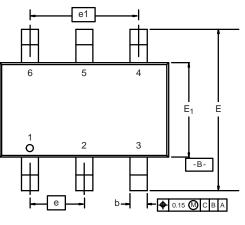
Normalized Thermal Transient Impedance, Junction-to-Foot



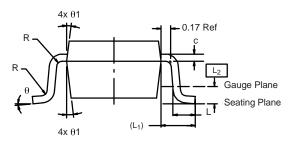
TSOP: 5/6–LEAD JEDEC Part Number: MO-193C







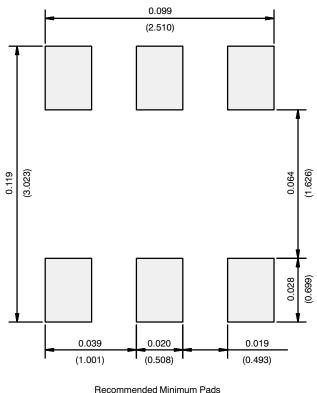
6-LEAD TSOP



	MILLIMETERS			INCHES			
Dim	Min	Nom	Мах	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref		0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							



RECOMMENDED MINIMUM PADS FOR TSOP-6



Recommended Minimum Pads Dimensions in Inches/(mm)



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