

HY1620P-VB Datasheet

N-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)	
200	0.046 at V _{GS} = 15 V	50	57	
	0.048 at V _{GS} = 10 V	46	57	

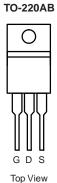
FEATURES

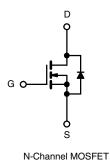
- TrenchFET[®] Power MOSFETS
- 175 °C Junction Temperature
- 100 % $\rm R_g$ and UIS Tested

APPLICATIONS

- Power Supply
- Lighting Systems







ABSOLUTE MAXIMUM RATING	S T _A = 25 °C, unless oth	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	os 200	
Gate-Source Voltage		V _{GS}	± 25	- V
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	1-	50	
	T _C = 100 °C	I _D	30	_
Pulsed Drain Current		I _{DM}	150	— A
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	20	
Single Pulse Avalanche Energy ^a	L = 0.1 mm	E _{AS}	20	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	P	166 ^b	
	T _A = 25 °C ^c	– P _D –	3.12	- W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	0.75		

Notes:

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).

Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	200			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	2		4	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	
	I _{DSS}	V_{DS} = 200 V, V_{GS} = 0 V, T_{J} = 125 °C			50	μA
		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	40			А
Drain-Source On-State Resistance ^b		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		0.048		
	Р	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}, \text{ T}_{J} = 125 \text{ °C}$		0.050		Ω
	R _{DS(on)}	V _{GS} = 10 V, I _D = 3 A, T _J = 175 °C		0.070		
		$V_{GS} = 6 V, I_D = 3 A$		0.092		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 3 A		35		S
Dynamic ^a	•	••				
Input Capacitance	C _{iss}			3000		pF
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, F = 1 MHz		180		
Reverse Transfer Capacitance	C _{rss}			80		
Total Gate Charge ^c	Qg			34	51	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		8		
Gate-Drain Charge ^c	Q _{gd}			12		
Gate Resistance	R _g		0.5		2.9	Ω
Turn-On Delay Time ^c	t _{d(on)}			15	25	
Rise Time ^c	t _r	V_{DD} = 100 V, R _L = 5.2 Ω I _D \cong 3 A, V _{GEN} = 10 V, R _g = 2.5 Ω		50	75	ns
Turn-Off Delay Time ^c	t _{d(off)}			30	45	
Fall Time ^c	t _f			60	90	
Source-Drain Diode Ratings and Char	acteristics (1	Γ _C = 25 °C)		<u>.</u>		
Pulsed Current	I _{SM}				36	А
Diode Forward Voltage ^b	V _{SD}	I _F = 3 A, V _{GS} = 0 V		0.9	1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 3 A, dl/dt = 100 A/μs		180	250	ns

Notes:

a. Guaranteed by design, not subject to production testing.

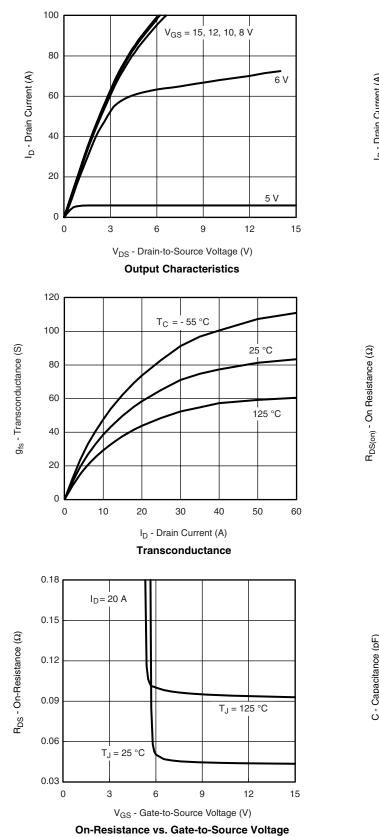
b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. Independent of operating temperature.

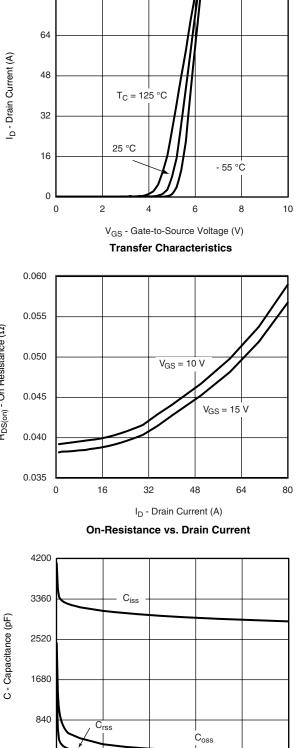
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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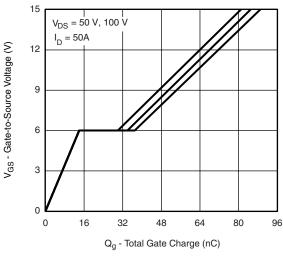
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



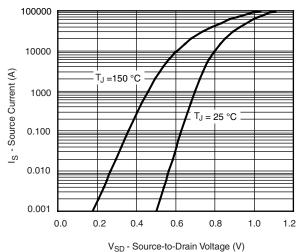
V_{DS} - Drain-to-Source Voltage (V) Capacitance

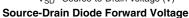


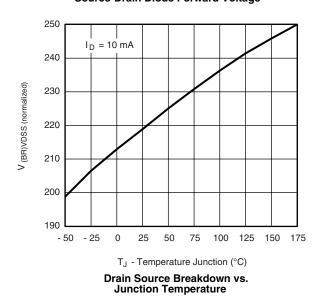


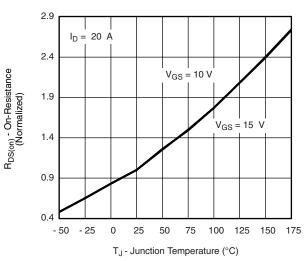




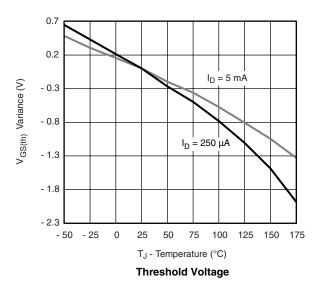


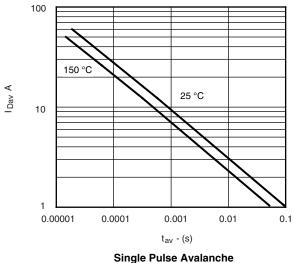


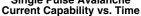




On-Resistance vs. Junction Temperature

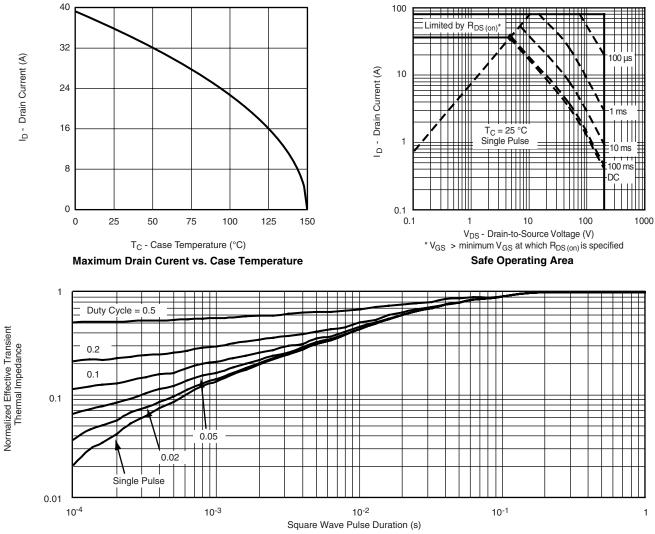








THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



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