

HM60N20-VB Datasheet

N-Channel 200 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}$ (Ω) I_D (A)		Q _g (TYP.)		
200	0.017 at V_{GS} = 10 V	80	64 nC		
200	0.018 at V_{GS} = 7.5 V	78	04 110		





FEATURES

- ThunderFET® power MOSFET
- Maximum 175 °C junction temperature
- 100 % R_g and UIS tested



COMPLIANT HALOGEN

D

GC

N-Channel MOSFET

APPLICATIONS

- Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power supplies
 - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \text{ °C}$, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	200	V			
Gate-Source Voltage	V _{GS}	± 20	V			
	T _C = 25 °C		80	Α		
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C	I _D	65			
Pulsed Drain Current (t = 100 μ s)	I _{DM}	240	A			
Avalanche Current	L = 0.1 mH	I _{AS}	60			
Single Avalanche Energy ^a		E _{AS}	180	mJ		
Maximum Bower Discipation a	$T_C = 25 \ ^\circ C$	р	375 ^b	w		
Maximum Power Dissipation ^a	T _C = 125 °C	- P _D	125 ^b			
Operating Junction and Storage Temperature	Range	T _J , T _{stg}	-55 to +175	°C		

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)	R _{thJC}	0.4	0/10		

Notes

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR4 material).

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \ \mu A$	200	-	-		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2	-	4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 250	nA	
		$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 200 V, V_{GS} = 0 V, T_{J} = 125 °C	-	-	150		
-		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	5	mA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \geq 10 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	90	-	-	Α	
Durin Course On Otata Desistance 3		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	0.017	-	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V_{GS} = 7.5 V, I_{D} = 30 A	-	0.018	-		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	75	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	4132	-	pF	
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 100 V, f = 1 MHz	-	246	-		
Reverse Transfer Capacitance	C _{rss}		-	21	-		
Total Gate Charge ^c	Qg		-	64	96	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 60 \text{ A}$	-	16.7	-		
Gate-Drain Charge ^c	Q _{gd}		-	16.9	-		
Gate Resistance	R _g	f = 1 MHz	1.5	3	5	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	13	26	ns	
Rise Time ^c	t _r	$\begin{array}{l} V_{DD} = 100 \; V, \; R_{L} = 1.66 \; \Omega \\ I_{D} \cong 60 \; A, \; V_{GEN} = 10 \; V, \; R_{g} = 1 \; \Omega \end{array}$	-	112	200		
Turn-Off Delay Time ^c	t _{d(off)}		-	35	70		
Fall Time ^c	t _f		-	80	150		
Drain-Source Body Diode Ratings an	nd Characteri	stics ^b (T _C = 25 °C)					
Pulsed Current (t = 100 µs)	I _{SM}		-	-	240	Α	
Forward Voltage ^a	V _{SD}	$I_F = 10 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.8	1.2	V	
Reverse Recovery Time	t _{rr}		-	160	320	ns	
Peak Reverse Recovery Charge	I _{RM(REC)}	I _F = 30 A, di/dt = 100 A/μs	-	11	20	А	
Reverse Recovery Charge	Q _{rr}		-	0.9	1.8	μC	

Notes

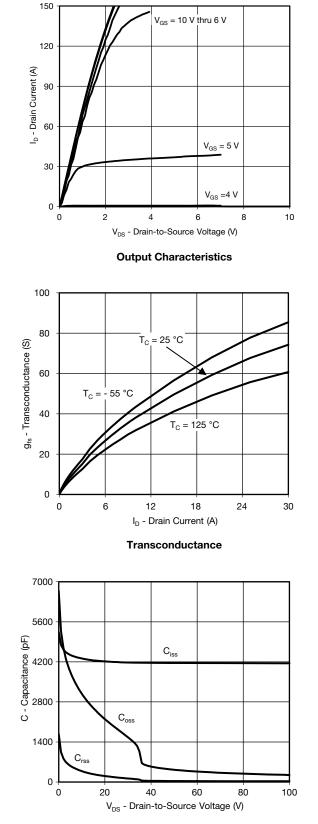
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

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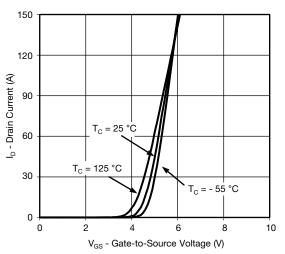
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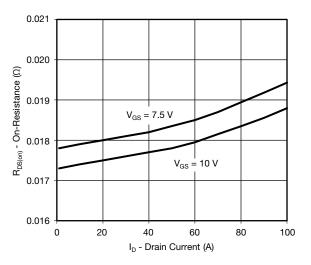
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

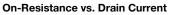


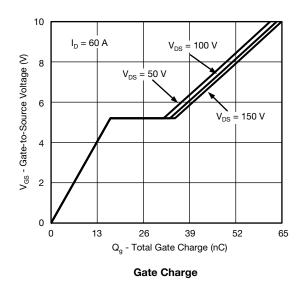
Capacitance



Transfer Characteristics



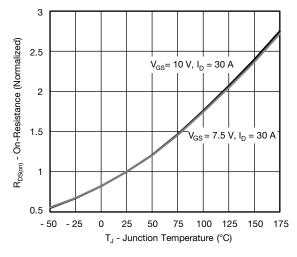




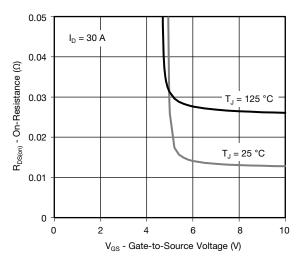
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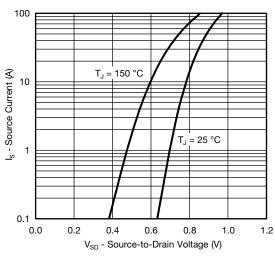
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



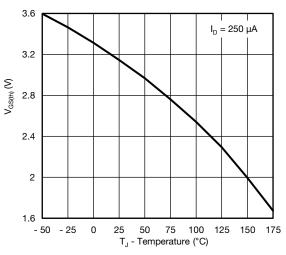
On-Resistance vs. Junction Temperature



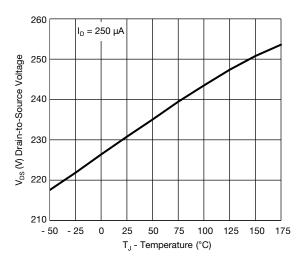
On-Resistance vs. Gate-to-Source Voltage



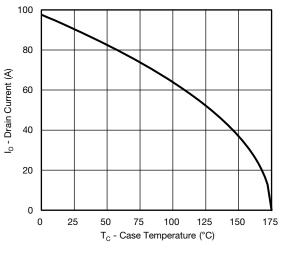
Source Drain Diode Forward Voltage



Threshold Voltage



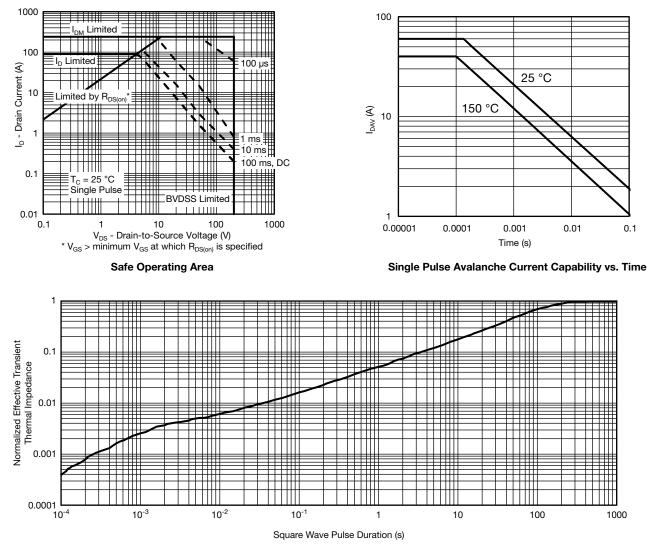
Drain Source Breakdown vs. Junction Temperature



Current De-rating



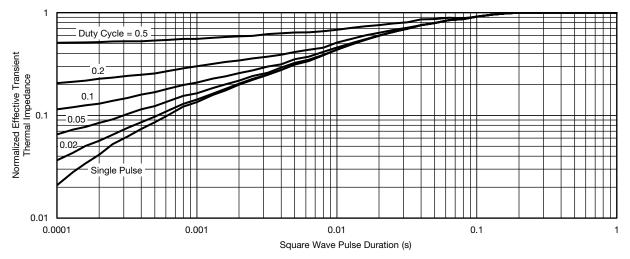
THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

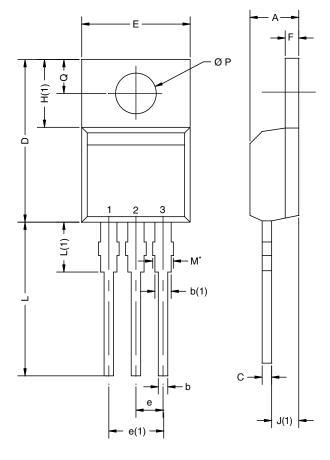
- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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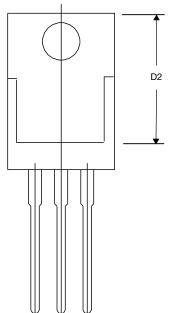
TO-220AB



	MILLIM	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
D2	12.19	12.70	0.480	0.500	
E	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471					

Note

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM





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