

RoHS

COMPLIANT

HALOGEN FREE

F9241-VB Datasheet

P-Channel 150 V (D-S) MOSFET

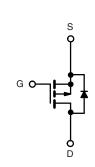
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω) Ι _D (Α		Q _g (Typ.)		
- 150	0.100 at V _{GS} = - 10 V	- 20	13.7		
- 150	0.120 at V_{GS} = - 4.5 V	- 18	13.7		

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET ٠
- 100 % R_g and UIS Tested
 Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Power Switch
- DC/DC Converters



P-Channel MOSFET

ABSOLUTE MAXIMUM RATING	S T _C = 25 °C, unless other	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 150			
Gate-Source Voltage		V _{GS} ± 20		V	
Continuous Drain Current ($T_J = 150 \ ^{\circ}C$)	T _C = 25 °C	L_	- 20	4	
	T _C = 70 °C	I _D	- 16		
Pulsed Drain Current		I _{DM}	- 60	- A -	
Avalanche Current		I _{AS}	- 18		
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	17.2	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	Р	37.1 ^b	w	
	T _A = 25 °C ^c	– P _D –	2.5		
Operating Junction and Storage Temperature R	ange	T _J , T _{stg}	- 55 to 150	°C	

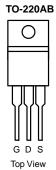
THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	50	°C/W		
Junction-to-Case (Drain)	R _{thJC}	3.9	0/1		

Notes:

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).





Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_D = -250 \mu A$ - 150				v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 2.5	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 250	nA	
		$V_{DS} = -100 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = - 150 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	μΑ	
		$V_{DS} = -150 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$			- 250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 10 V, V_{GS} = - 10 V	- 20			А	
	Р	V _{GS} = - 10 V, I _D = - 5.0A		0.100		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 4.0 A		0.120			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 5.0 A		12		S	
Dynamic ^b		· · · ·					
Input Capacitance	C _{iss}			1055		pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = - 75 V, f = 1 MHz		65			
Reverse Transfer Capacitance	C _{rss}	1		41			
Tatal Cata Charge	Qg	$V_{DS} = -75V, V_{GS} = -10V, I_{D} = -5.0A$		23.2	34.8	nC	
Total Gate Charge ^c				13.7	19.6		
Gate-Source Charge ^c	Q _{gs}	V_{DS} = - 75 V, V_{GS} = - 4.5 V, I_{D} = - 5.0 A		4.5			
Gate-Drain Charge ^c	Q _{gd}			5.8		l	
Gate Resistance	Rg	f = 1 MHz	1.2	5.7	11.5	Ω	
Turn-On Delay Time ^c	t _{d(on)}			7	14		
Rise Time ^c	t _r	V_{DD} = - 75 V, R_L = 17.2 Ω		12	18	ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 2.9 A, V_{GEN} = - 10 V, R_g = 1 Ω		33	50		
Fall Time ^c	t _f]		9	18		
Drain-Source Body Diode Ratings a	nd Characteri	stics T _C = 25 °C ^b					
Continuous Current	۱ _S				- 8.8	٨	
Pulsed Current	I _{SM}				- 15	A	
Forward Voltage ^a	V _{SD}	$I_{F} = -2.9 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.8	- 1.5	V	
Reverse Recovery Time	t _{rr}			50	75	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 2.9 A, dl/dt = 100 A/μs		- 4	- 6	Α	
Reverse Recovery Charge	Q _{rr}	1 1		98	147	nC	

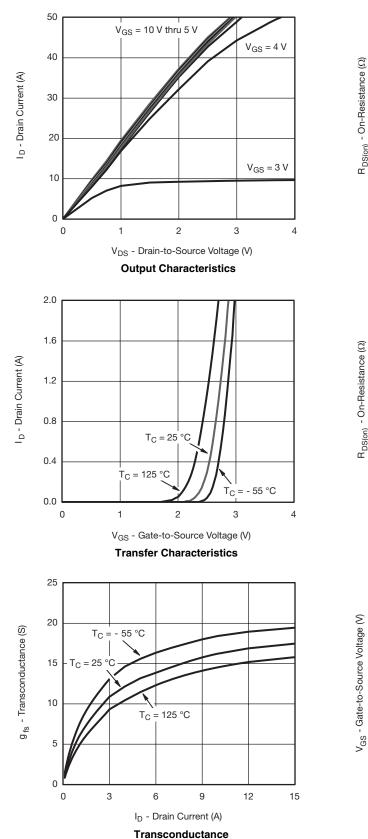
Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

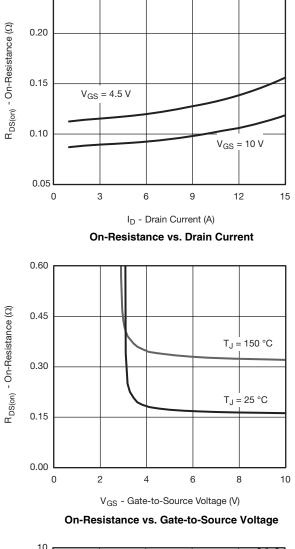
b. Guaranteed by design, not subject to production testing.c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

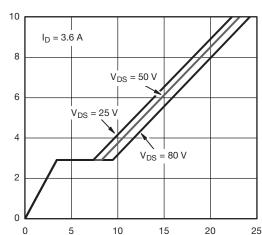




TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



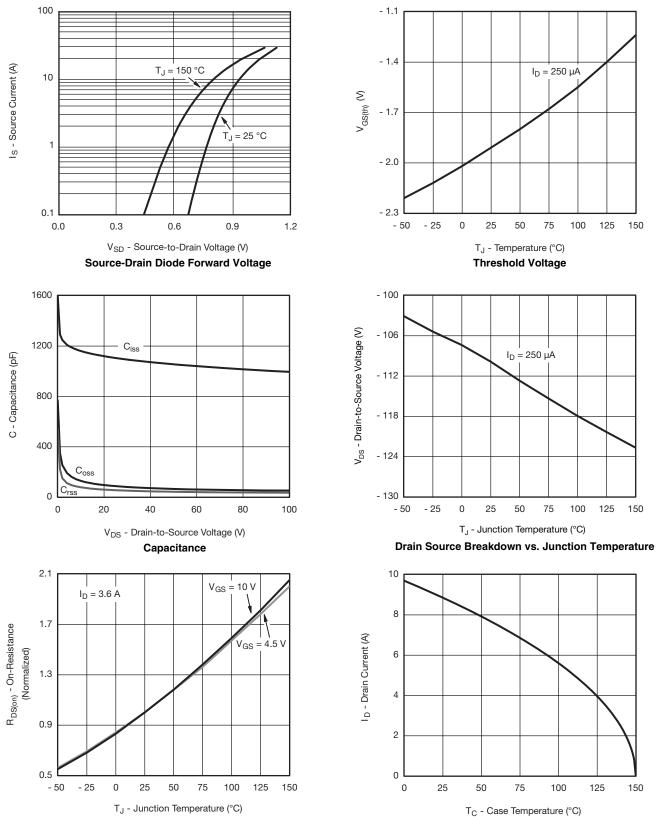
0.25



25



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

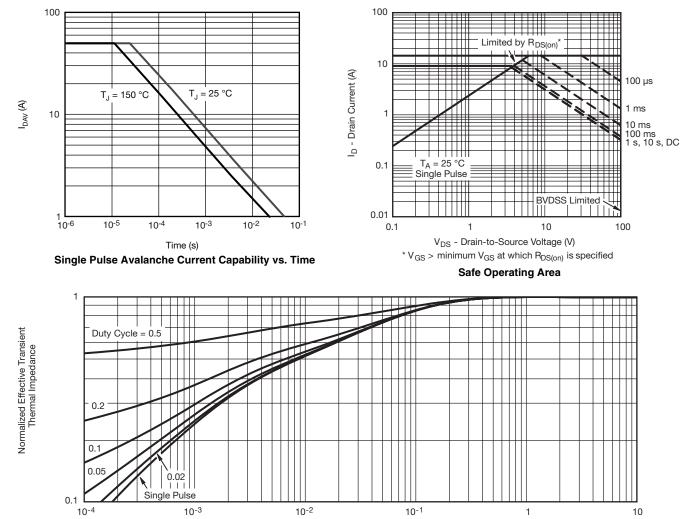


On-Resistance vs. Junction Temperature

Current Derating



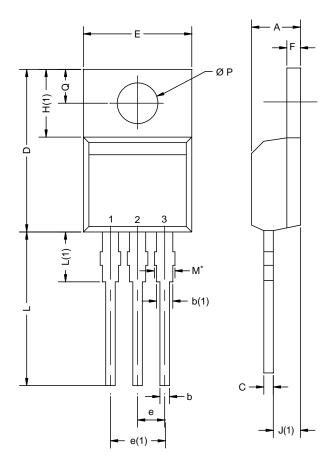
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Case



TO-220AB



	MILLIM	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
E	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471					

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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