

ST3422AS23RG-VB Datasheet N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}$ (Ω) I_{D} (A) ^a		Q _g (Typ.)	
60	0.030 at V _{GS} = 10 V	5.5	2.3 nC	
60	0.033 at V _{GS} = 4.5 V	4.5	2.3 110	

3 D

TO-236 (SOT23)

Top View

G 1

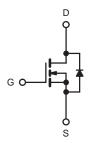
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FEATURES

- Halogen-free According to IEC 61249-2-21
 Available
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested

APPLICATIONS

- Battery Switch
- DC/DC Converter



S N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V _{DS}	60	V			
Gate-Source Voltage	V _{GS}	± 20				
	T _C = 25 °C		5.5			
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C	I_	4.5			
Continuous Drain Current (1j = 100°C)	T _A = 25 °C	I _D	3.9 ^{b, c}			
	T _A = 70 °C		3.2 ^{b, c}	^		
Pulsed Drain Current	I _{DM}	20	— A			
Continuous Source Drain Diado Current	T _C = 25 °C	1-	1.39			
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	0.91 ^{b, c}			
Avalanche Current L = 0.1 mH		I _{AS}	6			
Single-Pulse Avalanche Energy		E _{AS}	1.8	mJ		
	T _C = 25 °C		1.66			
Maximum Dawar Dissinction	T _C = 70 °C	Б	1.06	w		
Maximum Power Dissipation	T _A = 25 °C	P _D	1.09 ^{b, c}	vv		
	T _A = 70 °C		0.7 ^{b, c}			
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C			

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	≤5 s	R _{thJA}	90	115	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	60	75	0/11

Notes:

a. Based on $T_C = 25 \,^{\circ}C$.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under Steady State conditions is 130 °C/W.



COMPLIANT HALOGEN FREE Available

J.	3	® VBs	emi
W	ww.\	/Bsem	i.com

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	1 -			1	1	1	
Drain-Source Breakdown Voltage	V _{DS}	V _{DS} = 0 V, I _D = 250 μA	60			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			55		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5		mv/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current	1	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \geq 5$ V, V_{GS} = 10 V	8			Α	
	Б	V _{GS} = 10 V, I _D = 1.9 A		0.030		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 1.7 \text{ A}$		0.033			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15V, I _D = 1.9 A		5		S	
Dynamic ^b							
Input Capacitance	C _{iss}			190			
Output Capacitance	C _{oss}			26		pF	
Reverse Transfer Capacitance	C _{rss}	$V_{DS} = 30 V$, $V_{GS} = 0 V$, f = 1 MHz		15			
Total Gate Charge	Qg	$V_{DS} = 30$ V, $V_{GS} = 10$ V, $I_{D} = 1.9$ A		4.5	6.8	nC	
				2.3	3.5		
Gate-Source Charge	Q _{gs}	$V_{\rm DS}$ = 30 V, $V_{\rm GS}$ = 4.5 V, $I_{\rm D}$ = 1.9 A		0.8			
Gate-Drain Charge	Q _{gd}			1			
Gate Resistance	Rg	f = 1 MHz	0.6	2.8	5.6	Ω	
Turn-On Delay Time	t _{d(on)}			4	6		
Rise Time	t _r	V_{DD} = 30 V, R _L = 20 Ω		10	15	- ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ 1.5 A, V_{GEN} = 10 V, R_{G} = 1 Ω		10	15		
Fall Time	t _f			7	10.5		
Turn-On Delay Time	t _{d(on)}			15	23		
Rise Time	t _r	V_{DD} = 30 V, R _L = 20 Ω		16	24	- ns	
Turn-Off Delay Time	t _{d(off)}	I_{D} = 1.5 A, V_{GEN} = 4.5 V, R_{G} = 1 Ω		11	17		
Fall Time	t _f			11	17		
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			2.39	A	
Pulse Diode Forward Current ^a	I _{SM}				8		
Body Diode Voltage	V _{SD}	I _S = 1.5 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			15	23	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 1.5 A, dl/dt = 100 A/μs, T _J = 25 °C		10	15	nC	
Reverse Recovery Fall Time	t _a	$r_F = 1.5 \text{ A}, \text{ unut} = 100 \text{ A/} \mu\text{s}, 1\text{ J} = 25 ^{\circ}\text{C}$		12			
Reverse Recovery Rise Time	t _b			3		ns	

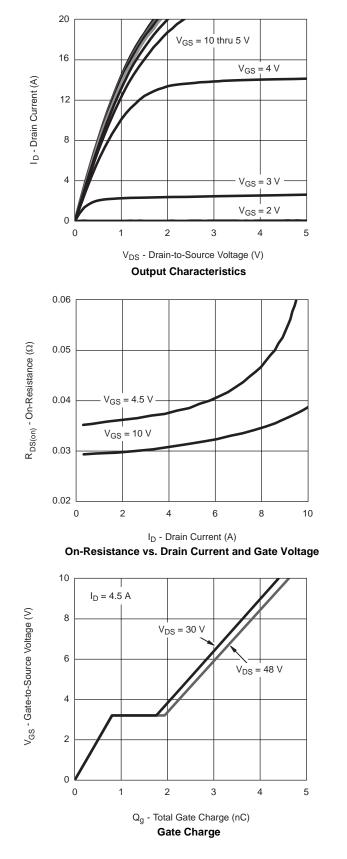
Notes:

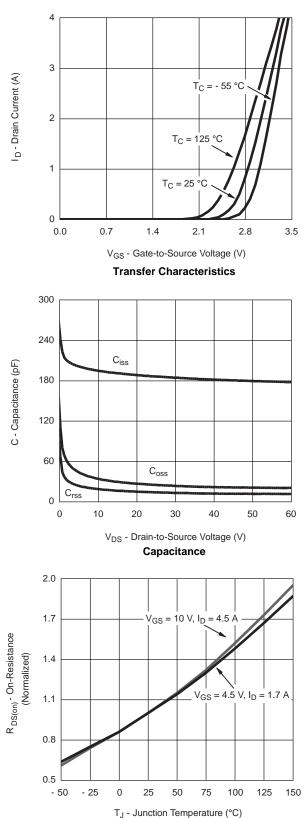
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



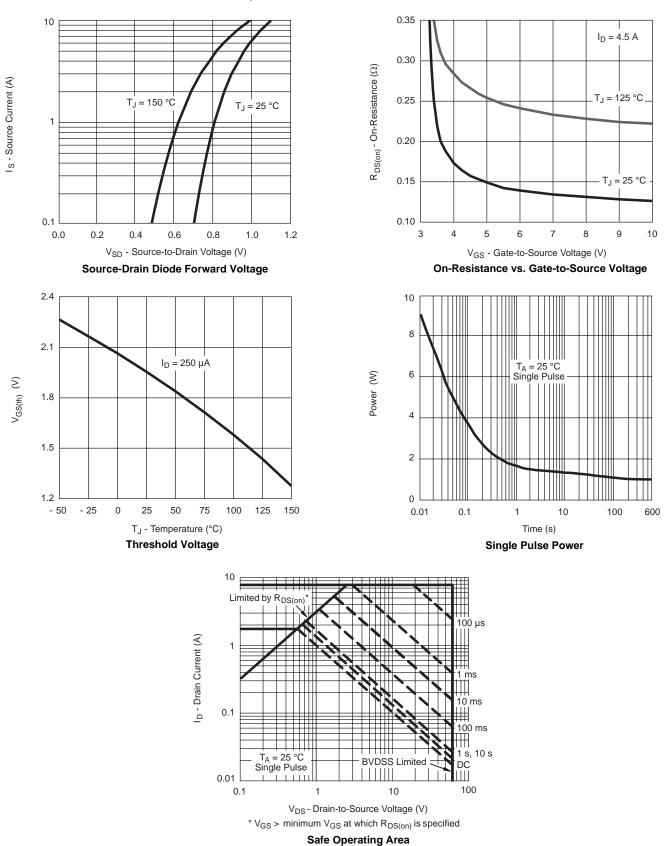
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





On-Resistance vs. Junction Temperature

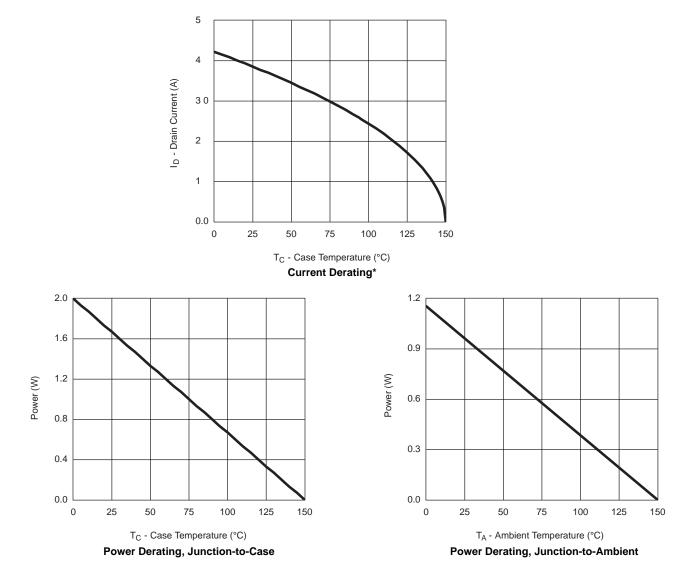




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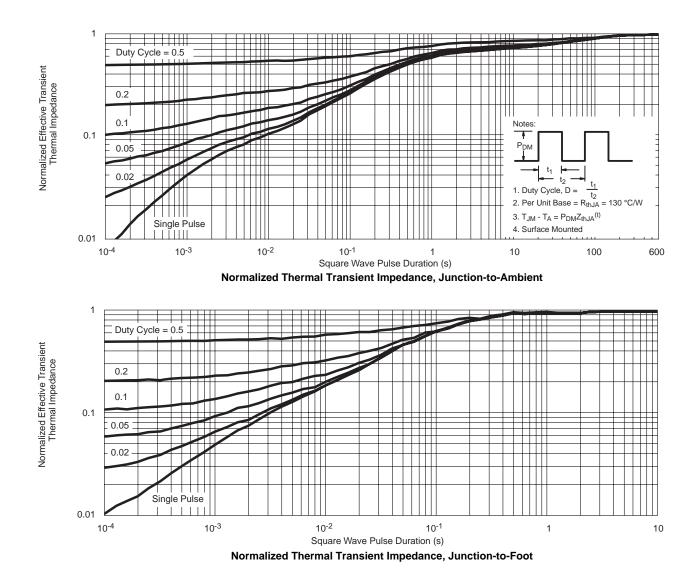
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



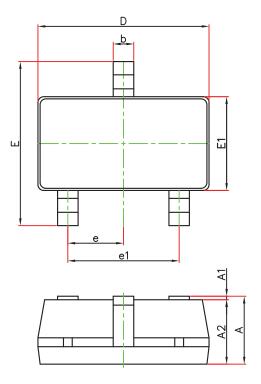
THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)

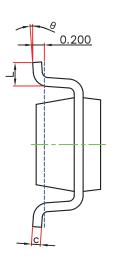


ST3422AS23RG-VB



SOT-23-3L



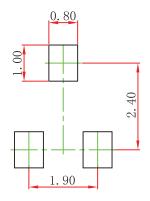


Symbol	Dimensions In	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E1	1.500	1.700	0.059	0.067	
E	2.650	2.950	0.104	0.116	
е	0.950(BSC)		0.037((BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

ST3422AS23RG-VB



RECOMMENDED MINIMUM PADS FOR SOT-23-3L



Note:

Controlling dimension:in millimeters.
 General tolerance:± 0.05mm.
 The pad layout is for reference purposes only.



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