

MT3414-VB Datasheet

N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^e	Q _g (Typ.)			
	0.028 at V_{GS} = 4.5 V	6 ^a				
20	0.042 at V_{GS} = 2.5 V	6 ^a	8.8 nC			
	0.050 at V _{GS} = 1.8 V	5.6				

SOT-23

G

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FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- DC/DC Converters
- Load Switch for Portable Applications

	A,	unless otherwise			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V _{GS}	± 12	v	
	T _C = 25 °C		6 ^a		
Continuous Drain Current ($T_{I} = 150 \ ^{\circ}C$)	T _C = 70 °C		5.1		
Continuous Drain Current $(1) = 150^{\circ}$ C)	T _A = 25 °C	I _D	5 ^{b, c}		
	T _A = 70 °C		4 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	20		
Continuous Source-Drain Diode Current	T _C = 25 °C		1.75		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1.04 ^{b, c}		
	T _C = 25 °C		2.1		
Maximum Dower Discinction	T _C = 70 °C		1.3	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	1.25 ^{b, c}	vv	
	T _A = 70 °C	1	0.8 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera	Ĭ	260	·U		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	80	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	60	0/10	

Notes:

a. Package limited

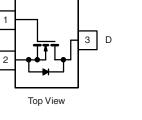
b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 125 °C/W.

e. Based on T_C = 25 °C.

FREE



SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	1 1	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	20			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		25		m)//°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 2.6		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.45		1.0	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA
	1	$V_{DS} = 20 V, V_{GS} = 0 V$			1	
Zero Gate Voltage Drain Current	DSS	V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 70 °C			10	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS}{\leq}5$ V, $V_{GS}{=}4.5$ V	20			A
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 5.0 \text{ A}$		0.028		
Drain-Source On-State Resistance ^a	Symbol V_{DS} $\Delta V_{DS}/T_J$ $\Delta V_{GS(th)}/T_J$ $\Delta V_{GS(th)}/T_J$ $V_{GS(th)}/T_J$ I_{DSS} I_{DSS} $I_{D(on)}$ $I_{D(on)}$ $I_{D(on)}$ $I_{D(on)}$ $I_{D(on)}$ $I_{D(on)}$ $I_{D(on)}$ $I_{D(on)}$ $I_{D(on)}$ I_{Ciss} Q_{fs} Q_{fs} Q_{gs} Q_{gd} Q_{gd} $I_{d(on)}$ I_r I_{SM} V_{SD} ime I_{rr} <tr< td=""><td>$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 4.7 \text{ A}$</td><td></td><td>0.042</td><td></td><td rowspan="2">Ω</td></tr<>	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 4.7 \text{ A}$		0.042		Ω
		$V_{GS} = 1.8 \text{ V}, \text{ I}_{D} = 4.3 \text{ A}$		0.050		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 5.0 \text{ A}$		24		S
Dynamic ^b						
Input Capacitance	C _{iss}			865		
Output Capacitance	C _{oss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz		105		pF
Reverse Transfer Capacitance	C _{rss}			55		
Total Gate Charge	0	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 5 \text{ V}, \text{ I}_{D} = 5.0 \text{ A}$		12	18	
				8.8	14	nC
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 5.0 A		1.1		110
Gate-Drain Charge	Q _{gd}			0.7		
Gate Resistance	Rg	f = 1 MHz	0.5	2.4	4.8	Ω
Turn-On Delay Time	t _{d(on)}			8	16	
Rise Time	t _r	$V_{DD} = 10 \text{ V}, \text{ R}_{L} = 2.2 \Omega$		17	26	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong \text{4}$ A, V_GEN = 4.5 V, R_g = 1 Ω		31	47	
Fall Time	t _f			8	16	ns
Turn-On Delay Time	t _{d(on)}			5	10	
Rise Time	t _r	$V_{DD} = 10 \text{ V}, \text{ R}_{L} = 2.2 \Omega$		13	20	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 4$ A, V_{GEN} = 5 V, R_g = 1 Ω		21	32	
Fall Time	t _f			6	12	
Drain-Source Body Diode Characteristic	1 1			1		
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			1.75	А
Pulse Diode Forward Current					20	
Body Diode Voltage	V _{SD}	$I_{S} = 4 A, V_{GS} = 0 V$		0.75	1.2	V
Body Diode Reverse Recovery Time				12	20	ns
Body Diode Reverse Recovery Charge		I _F = 4 A, dl/dt = 100 A/μs, T _J = 25 °C		5	10	nC
Reverse Recovery Fall Time		, ,, <u></u> , <u>-</u>		7		ns
Reverse Recovery Rise Time	t _b			5		

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

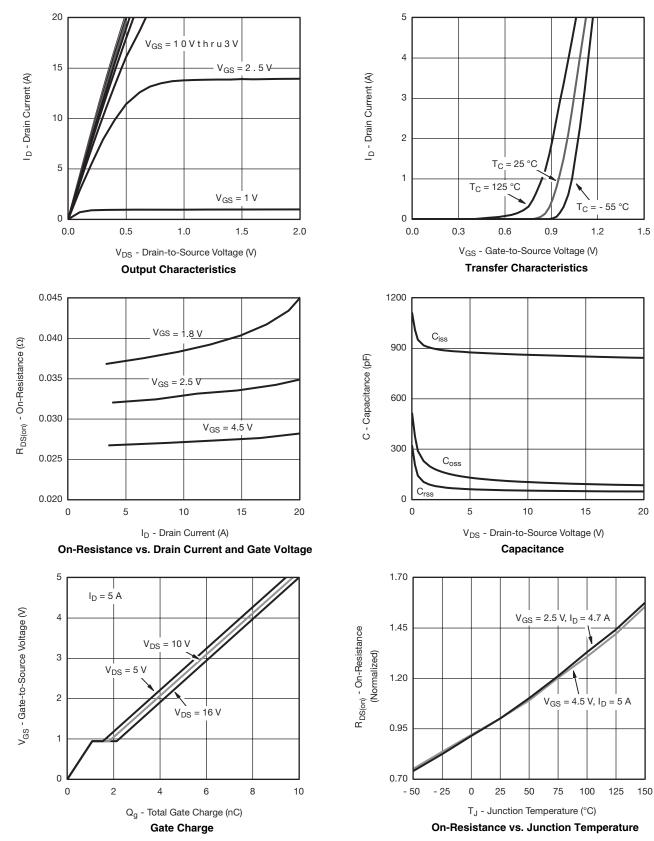
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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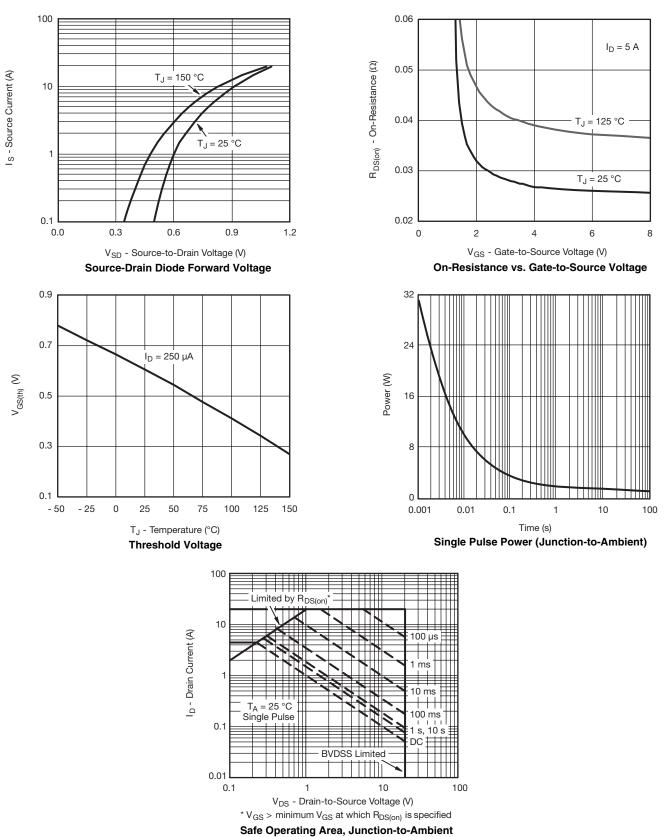
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



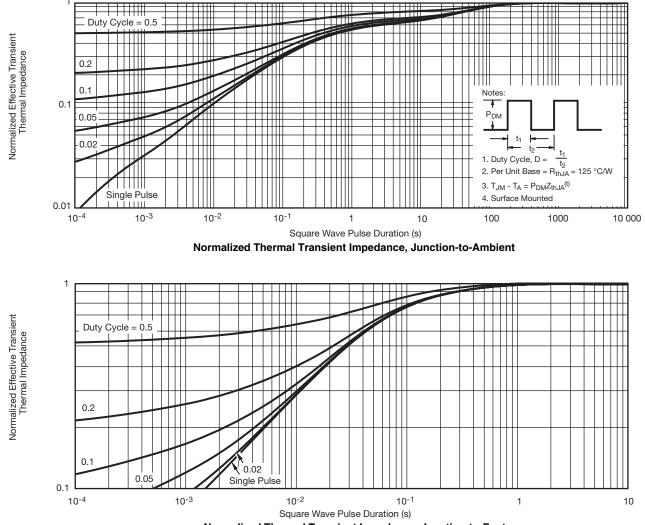
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



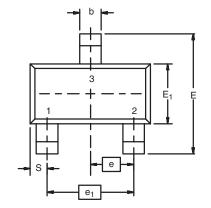
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot



SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES		
	Min	Мах	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
C	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01				



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)



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