

SI4435BDY-T1-E3-VB Datasheet P-Channel 30-V (D-S) MOSFET

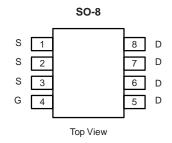
PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^d	Q _g (Typ.)			
- 30	0.018 at V _{GS} = - 10 V	- 9.0	13 nC			
- 30	0.024 at V_{GS} = - 4.5 V	- 7.8	13110			

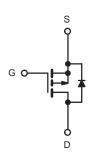
FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % Rg Tested

APPLICATIONS

- Load Switch
- Battery Switch





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T	A = 25 °C, unless othe	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 30	V	
Gate-Source Voltage	V _{GS}	± 20	v	
	T _C = 25 °C		- 9.0	
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C		- 7.2	
$Continuous Drain Current (1) = 150^{\circ} C)$	T _A = 25 °C		- 7.0 ^{a, b}	
	T _A = 70 °C	1	- 5.6 ^{a, b}	A
Pulsed Drain Current	I _{DM} - 3	- 30		
Octive Contraction Desire Disate Contract	T _C = 25 °C		- 3.5	
Continuous Source-Drain Diode Current	T _A = 25 °C	I I _S	- 2.1 ^{a, b}	
	T _C = 25 °C		4.2	
Mauianum Daura Diagingtian	T _C = 70 °C		2.7	w
Maximum Power Dissipation	T _A = 25 °C	P _D	2.5 ^{a, b}	VV
	T _A = 70 °C	1	1.6 ^{a, b}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	40	50	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	24	30	0,00	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. t = 10 s.

c. Maximum under Steady State conditions is 95 °C/W.

d. Based on $T_C = 25 \text{ °C}$.



HALOGEN

Available

SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μΑ		- 31		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			4.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		- 1			
Zero Gate Voltage Drain Current	IDSS	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \leq$ - 5 V, V_{GS} = - 10 V	- 20			А	
	_	V _{GS} = - 10 V, I _D = - 7.0 A		0.018			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 5.6 A		0.024		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 7.0 A		18		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1455			
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		180		pF	
Reverse Transfer Capacitance	C _{rss}			145		1	
	Q_g $V_{DS} = -15 V, V_{GS}$	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -7.0 \text{ A}$		25	38		
Total Gate Charge				13	20	nC	
Gate-Source Charge		$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7.0 \text{ A}$		3.5			
Gate-Drain Charge				5.5			
Gate Resistance	R _g	f = 1 MHz	0.4	2.0	4.0	Ω	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 2.7 Ω		13	20		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 5.6 A, V_{GEN} = - 10 V, R_g = 1 Ω		23	35		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			38	57	ns	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 2.7 Ω		89	134	1	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 5.6 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		22	33		
Fall Time	t _f			11	17		
Drain-Source Body Diode Characteris	tics	·		•	•		
Continous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 6.5	٨	
Pulse Diode Forward Current	I _{SM}				- 30	A	
Body Diode Voltage	V _{SD}	I _S = - 5.6 A, V _{GS} = 0 V		- 0.71	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			22	33	ns	
Body Diode Reverse Recovery Charge Q _{rr}				17	26	nC	
Reverse Recovery Fall Time	ta	$I_F = -5.6 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		13		-	
Reverse Recovery Rise Time	t _b	1		9		ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

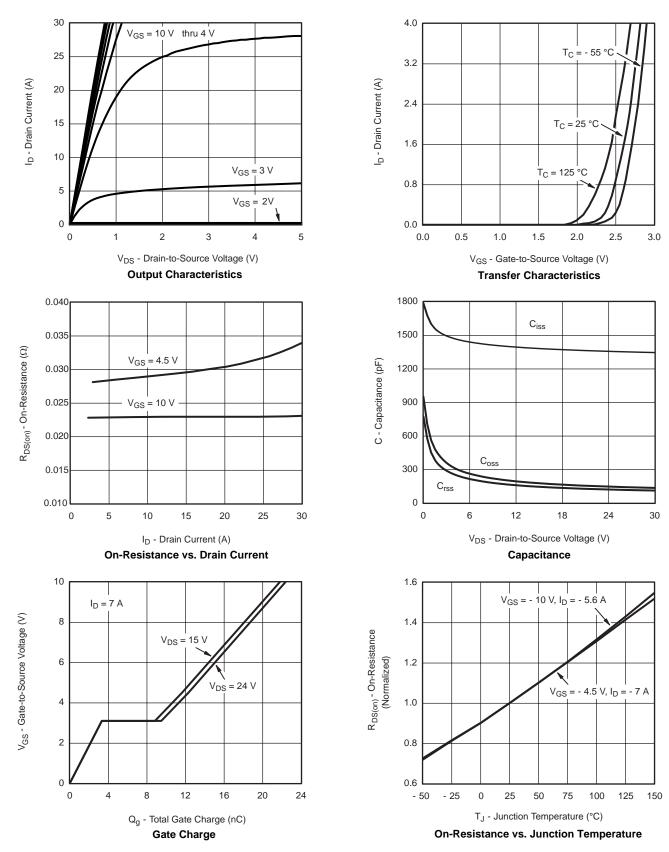
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Bsemi

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



服务热线:400-655-8788



0.05 100 I_D = 7 A 0.04 R $_{\text{DS(on)}}$ - On-Resistance ($\Omega)$ T_J = 125 °C Is - Source Current (A) 10 0.03 0.02 T_J = 150 °C T_J = 25 °C T_J = 25 °C 1 0.01 0.00 0.1 0 4 8 12 16 20 0.0 0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 1.0 1.1 V_{SD} - Source-to-Drain Voltage (V) V_{GS} - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Voltage Source-Drain Diode Forward Voltage 2.1 50 1.9 40 V_{GS(th)} (V) 1.7 30 Power (W) $I_D = 250 \ \mu A$ 1.5 20 1.3 10 1.1 0 - 50 - 25 0 25 50 75 100 125 150 0.001 0.01 0.1 10 100 1 T_J - Temperature (°C) Time (s) Single Pulse Power, Junction-to-Ambient **Threshold Voltage** 100 Limited by R_{DS(on)} 10 I_D - Drain Current (A) ms 10 ms 100 ms 0.1 S $T_A = 25 \degree C$ Single Pulse 10 s DC BVDSS Limited 0.01 0.1 10 100 1 V_{DS} - Drain-to-Source Voltage (V) * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

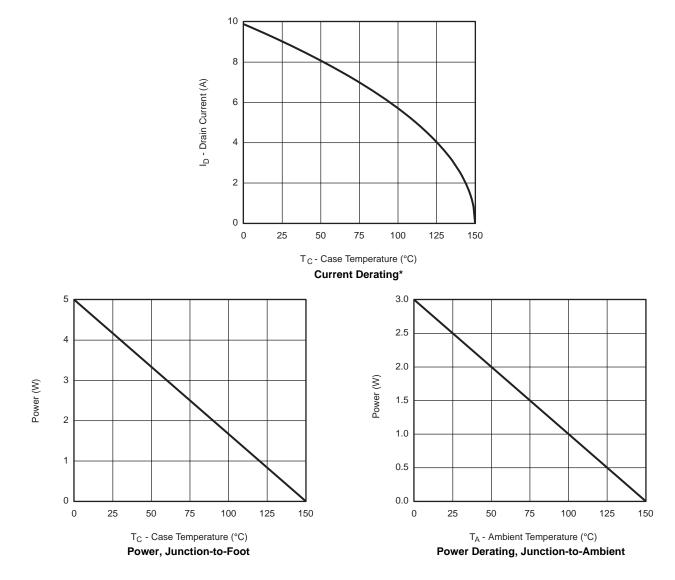
Safe Operating Area

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

服务热线:400-655-8788



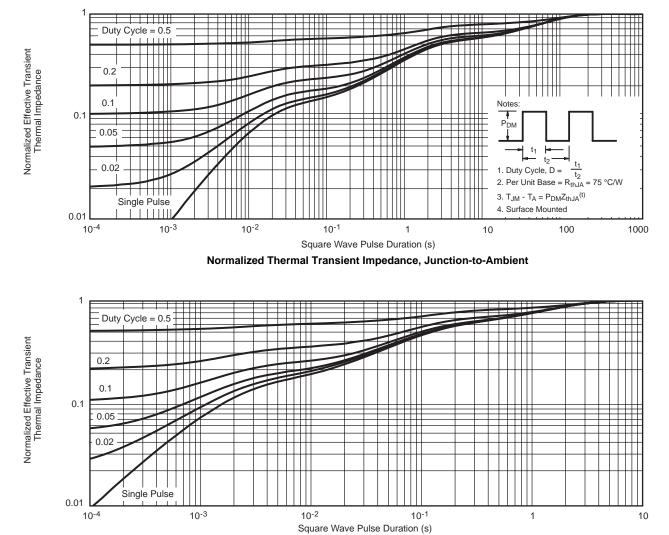
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

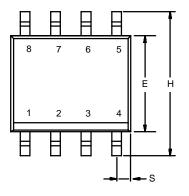


Normalized Thermal Transient Impedance, Junction-to-Foot



SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012

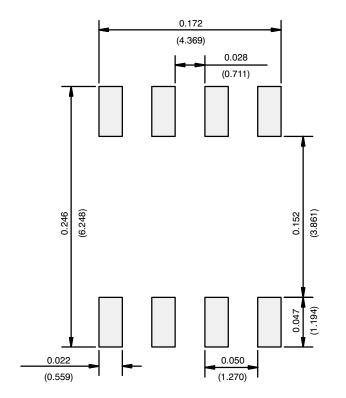




	MILLIMETERS		INC	HES	
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)



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