

AM4417P-T1-PF-VB Datasheet P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)			
- 60	0.0250 at V _{GS} = - 10 V	- 10	76 nC			
- 00	0.0280 at V _{GS} = - 4.5 V	- 9				

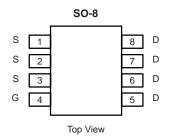
FEATURES

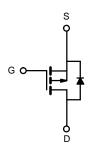
- TrenchFET® Power MOSFET
- 100 % UIS Tested

APPLICATIONS

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATING	S (T _A = 25 °C, unle	ess otherwise no	ted)		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 60	V		
Gate-Source Voltage		V_{GS}	± 20	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
	T _C = 25 °C		- 10 ^a		
Continuous Proin Current (T. – 450 °C)	T _C = 70 °C		- 9	1	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	8 ^b	1	
	T _A = 70 °C		- 8 ^b	A	
Pulsed Drain Current	I _{DM}	- 50	_		
Avalanche Current Pulse		I _{AS}	- 45	7	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	101	mJ	
Continuous Courses Brain Binds Coursest	T _C = 25 °C	1	69 ^a		
Continuous Source-Drain Diode Current	T _A = 25 °C	l _s —	2.1 ^b	Α Α	
	T _C = 25 °C		104.2 ^a		
Manianum Pausan Dinain ation	T _C = 70 °C		66.7 ^a		
Maximum Power Dissipation	T _A = 25 °C	P _D	3.1 ^b	W	
	T _A = 70 °C		2 ^b	1	
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	Steady State	R _{thJA}	33	40	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.98	1.2	- °C/vv	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	$I_{D} = -250 \mu\text{A}$		68		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	i _D = - 230 μA		- 5.2		inv/·C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valta na Busin Commant		V _{DS} = - 60 V, V _{GS} = 0 V			-1		
Zero Gate Voltage Drain Current	IDSS	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 55 °C			- 10	- 10 µA	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 12			Α	
	В	V _{GS} = - 10 V, I _D = - 5 A		0.0250		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 5A		0.0280			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 10 A	20			S	
Dynamic ^b							
Input Capacitance	C _{iss}			3500		pF	
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		390			
Reverse Transfer Capacitance	C _{rss}			290			
Tatal Cata Chausa	Qg	V _{DS} = -30 V, V _{GS} = -10 V, I _D = -55 A		76	115		
Total Gate Charge				38	60	1	
Gate-Source Charge	Q_{gs}	Q_{gs} $V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -55 \text{ A}$		16		nC	
Gate-Drain Charge	Q_{gd}			19			
Gate Resistance	R _g	f = 1 MHz		5.2		Ω	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	$V_{DD} = -2 \text{ V}, R_{L} = 2 \Omega$		7	15	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω		70	110		
Fall Time	t _f			40	60		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 9	_	
Pulse Diode Forward Current ^a	I _{SM}	М			- 15	A	
Body Diode Voltage	V_{SD}	I _S = - 30 A		- 1	- 1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			45	68	ns	
Body Diode Reverse Recovery Charge Q _{rr}		FO A di/d+ 400 A/: T 05 00		59	120	nC	
Reverse Recovery Fall Time	t _a	$I_F = -50 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		29		ns	
Reverse Recovery Rise Time	t _b			16			

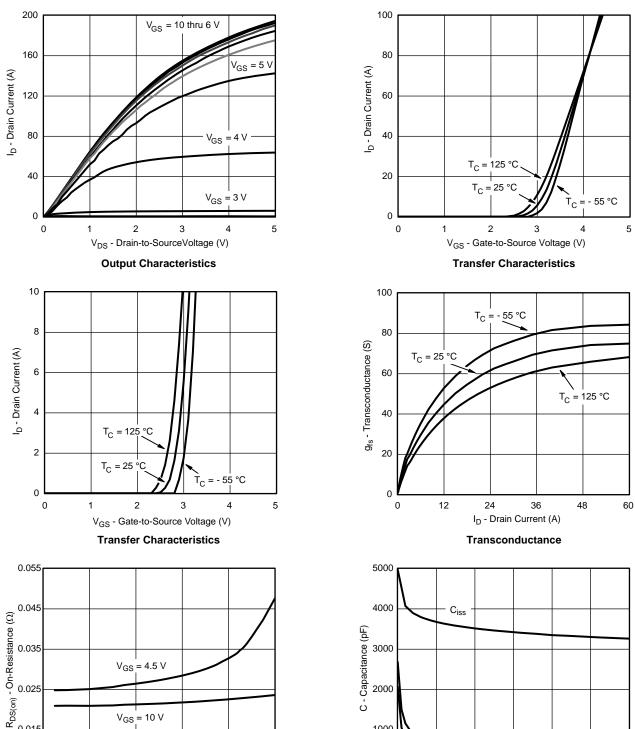
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



I_D - Drain Current (A) On-Resistance vs. Drain Current

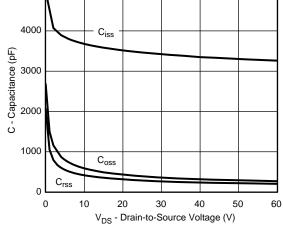
60

80

100

 $V_{GS} = 10 \text{ V}$

40



Capacitance

服务热线:400-655-8788

20

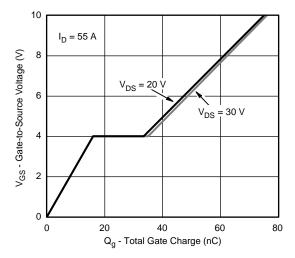
0.015

0.005

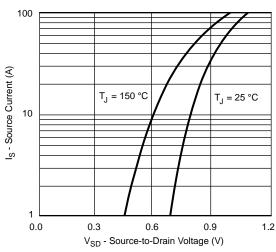
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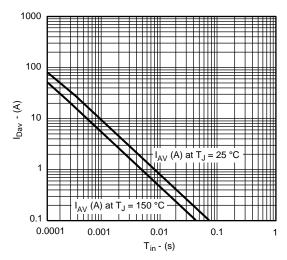
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



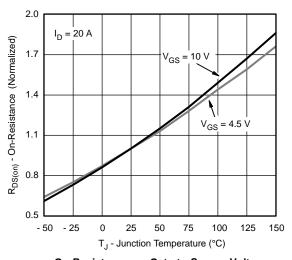
Gate Charge



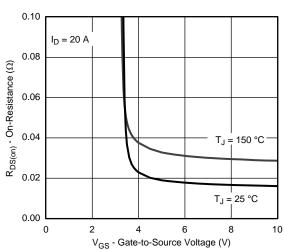
Source-Drain Diode Forward Voltage



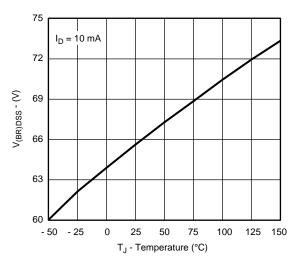
Single Pulse Avalanche Current Capability vs. Time



On-Resistance vs. Gate-to-Source Voltage



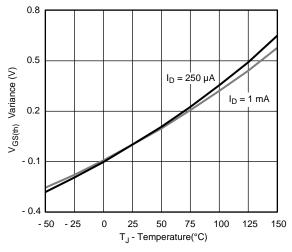
On-Resistance vs. Gate-to-Source Voltage

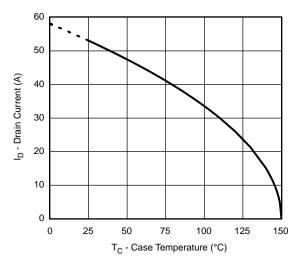


Drain-Source Breakdown Voltage vs. Junction Temperature

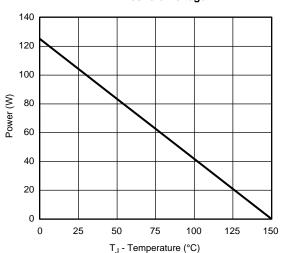


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

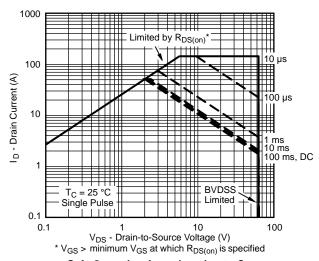




Threshold Voltage

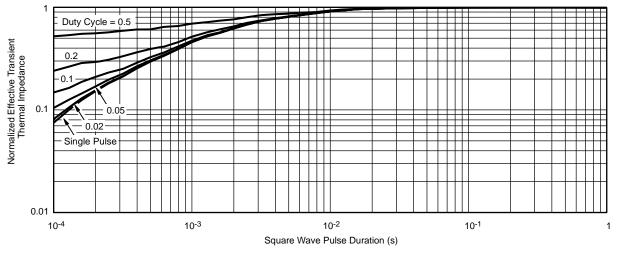


Max. Drain Current vs. Case Temperature



Power Derating, Junction-to-Case

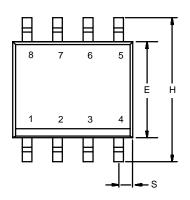


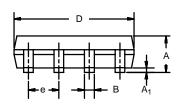


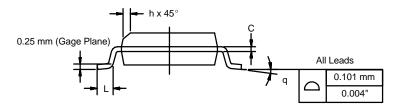
Normalized Thermal Transient Impedance, Junction-to-Case



SOIC (NARROW): 8-LEADJEDEC Part Number: MS-012







	MILLIM	IETERS	INC	HES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27 BSC		0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
FCN: C-06527-Rev I 11-Sen-06						

ECN: C-06527-Rev. I, 11-Sep-06

DWG: 5498



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